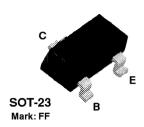


# BCV27



# **NPN Darlington Transistor**

This device is designed for applications requiring extremely high current gain at collector currents to 1.0 A. Sourced from Process 05.

**Absolute Maximum Ratings\*** 

TA = 25°C unless otherwise noted

Symbol	Parameter	Value	Units	
V <sub>CEO</sub>	Collector-Emitter Voltage	30	V	
V <sub>CBO</sub>	Collector-Base Voltage	40	V	
V <sub>EBO</sub>	Emitter-Base Voltage	10	V	
Ic	Collector Current - Continuous	1.2	A	
T <sub>J</sub> , T <sub>stg</sub>	Operating and Storage Junction Temperature Range	-55 to +150	°C	

<sup>\*</sup>These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

### **Thermal Characteristics**

TA = 25°C unless otherwise noted

Symbol	Characteristic	Max	Units
		*BCV27	
P <sub>D</sub>	Total Device Dissipation Derate above 25°C	350 2.8	mW mW/°C
ReJA	Thermal Resistance, Junction to Ambient	357	°C/W

<sup>\*</sup>Device mounted on FR-4 PCB 40 mm X 40 mm X 1.5 mm.

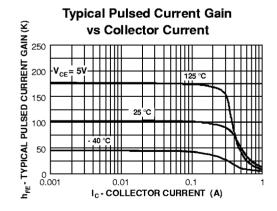
These ratings are based on a maximum junction temperature of 150 degrees C.
 These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations

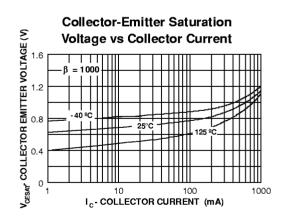
### **NPN Darlington Transistor**

(continued)

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
OFF CHA	RACTERISTICS					
V <sub>(BR)CEO</sub>	Collector-Emitter Breakdown Voltage	$I_{\rm C} = 10 \text{ mA}, I_{\rm B} = 0$	30			V
V <sub>(BR)CBO</sub>	Collector-Base Breakdown Voltage	$I_{C} = 10  \mu A, I_{E} = 0$	40			V
V <sub>(BR)EBO</sub>	Emitter-Base Breakdown Voltage	$I_E = 100 \text{ nA}, I_C = 0$	10			V
Ісво	Collector-Cutoff Current	V <sub>CB</sub> = 30 V, I <sub>E</sub> = 0			0.1	μА
I <sub>EBO</sub>	Emitter-Cutoff Current	$V_{EB} = 10 \text{ V}, I_{C} = 0$			0.1	μА
ON CHAP	RACTERISTICS DC Current Gain	$I_C = 1.0 \text{ mA}, V_{CE} = 5.0 \text{ V}$	4,000			Π
		$I_C = 10 \text{ mA}, V_{CE} = 5.0 \text{ V}$	10,000			
/	Collector-Emitter Saturation Voltage	$I_C = 100 \text{ mA}, V_{CE} = 5.0 \text{ V}$ $I_C = 100 \text{ mA}, I_R = 0.1 \text{ mA}$	20,000		1.0	V
	Collector-Emitter Saturation Voltage  Base-Emitter Saturation Voltage	$I_C = 100 \text{ mA}, V_{CE} = 5.0 \text{ V}$ $I_C = 100 \text{ mA}, I_B = 0.1 \text{ mA}$ $I_C = 100 \text{ mA}, I_B = 0.1 \text{ mA}$	20,000		1.0	V
V <sub>CE(sat)</sub> V <sub>BE(sat)</sub>	ļ <u> </u>	$I_C = 100 \text{ mA}, I_B = 0.1 \text{ mA}$	20,000			<u> </u>
BE(sat)	ļ <u> </u>	$I_C = 100 \text{ mA}, I_B = 0.1 \text{ mA}$	20,000			<u> </u>
V <sub>BE(sat)</sub>	Base-Emitter Saturation Voltage	$I_C = 100 \text{ mA}, I_B = 0.1 \text{ mA}$	20,000	220		<u> </u>

# **Typical Characteristics**

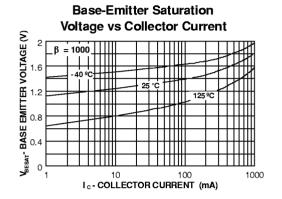


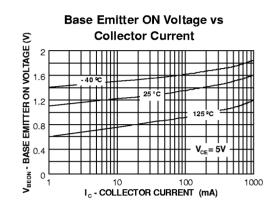


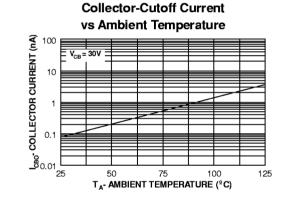
### **NPN Darlington Transistor**

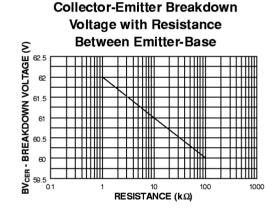
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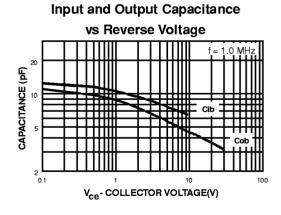
### Typical Characteristics (continued)

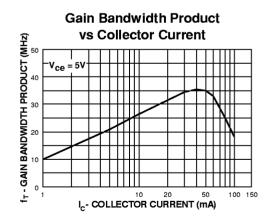










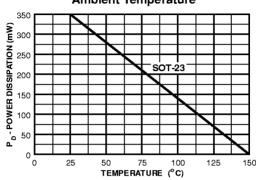


# **NPN Darlington Transistor**

(continued)

# Typical Characteristics (continued)





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CROSSVOLT<sup>TM</sup> POP<sup>TM</sup>

E²CMOS™ PowerTrench™

FACT<sup>TM</sup> QS<sup>TM</sup>

FACT Quiet Series  $^{\text{TM}}$  Quiet Series  $^{\text{TM}}$  SuperSOT  $^{\text{TM}}$ -3 FAST  $^{\text{TM}}$  SuperSOT  $^{\text{TM}}$ -6 GTO  $^{\text{TM}}$  SuperSOT  $^{\text{TM}}$ -8 HiSeC  $^{\text{TM}}$ 

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No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
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