SOD-123 Schottky Barrier Diodes

MMSD301T1G, SMMSD301T1G, MMSD701T1G, SMMSD701T1G,

The MMSD301T1, and MMSD701T1 devices are spin-offs of our popular MMBD301LT1, and MMBD701LT1 SOT-23 devices. They are designed for high-efficiency UHF and VHF detector applications. Readily available to many other fast switching RF and digital applications.

Features

- Extremely Low Minority Carrier Lifetime
- Very Low Capacitance
- Low Reverse Leakage
- AEC Qualified and PPAP Capable
- S Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant*

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Reverse Voltage MMSD301T1G, SMMSD301T1G MMSD701T1G, SMMSD701T1G	V _R	30 70	Vdc
Forward Current (DC) Continous	IF	200	mA
Forward Power Dissipation T _A = 25°C	P _F	225	mW
Junction Temperature	TJ	-55 to +125	°C
Storage Temperature Range	T _{stg}	-55 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



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SOD-123 CASE 425 STYLE 1



MARKING DIAGRAM



xx = Specific Device Code

XT = MMSD301T1G SMMSD301T1G

XH = MMSD701T1G SMMSD701T1G

M = Date Code

= Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping [†]
MMSD301T1G	SOD-123 (Pb-Free)	3,000 / Tape & Reel
SMMSD301T1G	SOD-123 (Pb-Free)	3,000 / Tape & Reel
MMSD701T1G	SOD-123 (Pb-Free)	3,000 / Tape & Reel
SMMSD701T1G	SOD-123 (Pb-Free)	3,000 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

^{*}For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

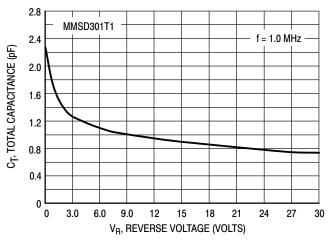
${\bf MMSD301T1G,\,SMMSD301T1G,\,MMSD701T1G,\,SMMSD701T1G,}$

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
Reverse Breakdown Voltage (I _R = 10 μA) MMSD301T1G, SMMSD301T1G	V _{(BR)R}	30	-	-	V
MMSD701T1G, SMMSD701T1G		70	1	_	
Diode Capacitance (V _B = 0 V, f = 1.0 MHz)	C _T				pF
MMSD301T1G, SMMSD301T1G MMSD701T1G, SMMSD701T1G		- -	0.9 0.5	1.5 1.0	
Total Capacitance	C _T				pF
(V _R = 15 V, f = 1.0 MHz) MMSD301T1G, SMMSD301T1G (V _R = 20 V, f = 1.0 MHz)		-	0.9	1.5	
MMSD701T1G, SMMSD701T1G		-	0.5	1.0	
Reverse Leakage	I _R				nAdc
(V _R = 25 V) MMSD301T1G, SMMSD301T1G (V _R = 35 V)		-	13	200	
MMSD701T1G, SMMSD701T1G		-	9.0	200	
Forward Voltage (I _F = 1.0 mAdc)	V _F				Vdc
MMSD301T1G, SMMSD301T1G (I _F = 10 mA) (I _F = 1.0 mAdc)		-	0.38 0.52	0.45 0.6	
MMSD701T1G, SMMSD701T1G (I _F = 10 mA)		- -	0.42 0.7	0.5 1.0	

MMSD301T1G, SMMSD301T1G, MMSD701T1G, SMMSD701T1G,

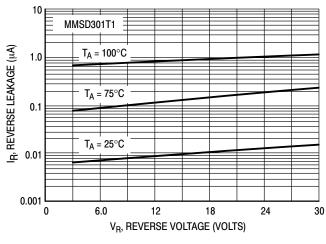
TYPICAL CHARACTERISTICS MMSD301T1G, SMMSD301T1G



500 MMSD301T1 WMSD301T1 WRAKAUER METHOD KRAKAUER METHOD 100 100 20 30 40 50 60 70 80 90 100 I_F FORWARD CURRENT (mA)

Figure 1. Total Capacitance

Figure 2. Minority Carrier Lifetime



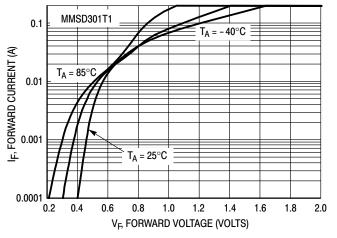
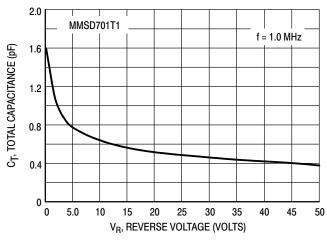


Figure 3. Reverse Leakage

Figure 4. Forward Voltage

MMSD301T1G, SMMSD301T1G, MMSD701T1G, SMMSD701T1G,

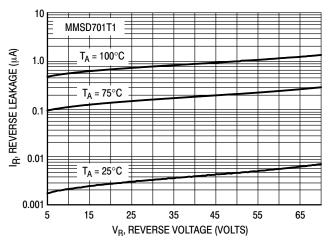
TYPICAL CHARACTERISTICS MMSD701T1G, SMMSD701T1G



MMSD701T1 τ , MINORITY CARRIER LIFETIME (ps) KRAKAUER METHOD I_{F.} FORWARD CURRENT (mA)

Figure 5. Total Capacitance

Figure 6. Minority Carrier Lifetime



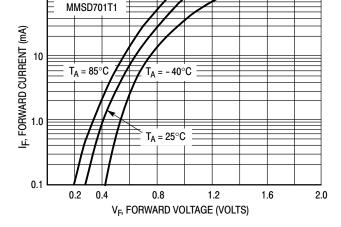


Figure 7. Reverse Leakage

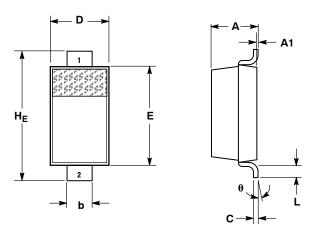
Figure 8. Forward Voltage



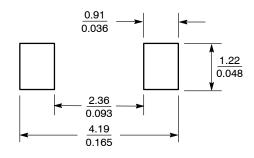
SOD-123 CASE 425-04 ISSUE G

DATE 07 OCT 2009





SOLDERING FOOTPRINT*

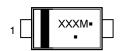


SCALE 10:1

- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI
- Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH.

	M	MILLIMETERS INCHES			MILLIMETERS			
DIM	MIN	NOM	MAX	MIN	NOM	MAX		
Α	0.94	1.17	1.35	0.037	0.046	0.053		
A1	0.00	0.05	0.10	0.000	0.002	0.004		
b	0.51	0.61	0.71	0.020	0.024	0.028		
С			0.15			0.006		
D	1.40	1.60	1.80	0.055	0.063	0.071		
Е	2.54	2.69	2.84	0.100	0.106	0.112		
HE	3.56	3.68	3.86	0.140	0.145	0.152		
L	0.25			0.010				
θ	0°		10°	0°		10°		

GENERIC MARKING DIAGRAM*



XXX = Specific Device Code

= Date Code

= Pb-Free Package

(Note: Microdot may be in either location)

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " •", may or may not be present.

STYLE 1: PIN 1. CATHODE 2. ANODE

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^{*}For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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