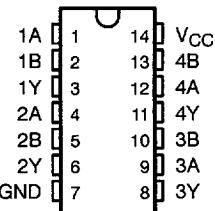


- **EPIC™ (Enhanced-Performance Implanted CMOS) Submicron Process**
- Typical  $V_{OLP}$  (Output Ground Bounce)  
 $< 0.8 \text{ V}$  at  $V_{CC} = 3.3 \text{ V}$ ,  $T_A = 25^\circ\text{C}$
- Typical  $V_{OHV}$  (Output  $V_{OH}$  Undershoot)  
 $> 2 \text{ V}$  at  $V_{CC} = 3.3 \text{ V}$ ,  $T_A = 25^\circ\text{C}$
- Package Options Include Plastic Small-Outline (D), Shrink Small-Outline (DB), and Thin Shrink Small-Outline (PW) Packages

D, DB, OR PW PACKAGE  
(TOP VIEW)



### description

This quadruple 2-input positive-AND gate is designed for 2.7-V to 3.6-V  $V_{CC}$  operation.

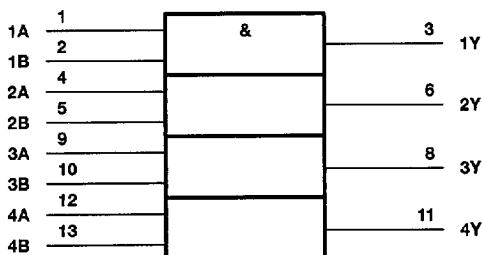
The SN74LVC08 performs the Boolean functions  $Y = A \bullet B$  or  $Y = \overline{A} + \overline{B}$  in positive logic.

The SN74LVC08 is characterized for operation from  $-40^\circ\text{C}$  to  $85^\circ\text{C}$ .

FUNCTION TABLE  
(each gate)

INPUTS		OUTPUT
A	B	Y
H	H	H
L	X	L
X	L	L

### logic symbol†



### logic diagram, each gate (positive logic)



† This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

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PRODUCT PREVIEW information concerns products in the formative or design phase of development. Characteristic data and other specifications are design goals. Texas Instruments reserves the right to change or discontinue these products without notice.

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# SN74LVC08

## QUADRUPLE 2-INPUT POSITIVE-AND GATE

SCAS283 – JANUARY 1993 – REVISED MARCH 1994

### absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

Supply voltage range, $V_{CC}$	.....	-0.5 V to 4.6 V
Input voltage range, $V_I$	.....	-0.5 V to 4.6 V
Output voltage range, $V_O$ (see Note 1)	.....	-0.5 V to $V_{CC} + 0.5$ V
Input clamp current, $I_{IK}$ ( $ V_I  < 0$ )	.....	-50 mA
Output clamp current, $I_{OK}$ ( $V_O < 0$ or $V_O > V_{CC}$ )	.....	±50 mA
Continuous output current, $I_O$ ( $V_O = 0$ to $V_{CC}$ )	.....	±50 mA
Continuous current through $V_{CC}$ or GND	.....	±100 mA
Maximum power dissipation at $T_A = 55^\circ\text{C}$ (in still air): D package	.....	1.25 W
DB or PW package	.....	0.5 W
Storage temperature range	.....	-65°C to 150°C

† Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTE 1: This value is limited to 4.6 V maximum.

### recommended operating conditions (see Note 2)

		MIN	MAX	UNIT
$V_{CC}$	Supply voltage	2.7	3.6	V
$V_{IH}$	High-level input voltage	$V_{CC} = 2.7$ V to 3.6 V	2	V
$V_{IL}$	Low-level input voltage	$V_{CC} = 2.7$ V to 3.6 V	0.8	V
$V_I$	Input voltage	0	$V_{CC}$	V
$V_O$	Output voltage	0	$V_{CC}$	V
$I_{OH}$	High-level output current	$V_{CC} = 2.7$ V	-12	mA
		$V_{CC} = 3$ V	-24	
$I_{OL}$	Low-level output current	$V_{CC} = 2.7$ V	12	mA
		$V_{CC} = 3$ V	24	
$\Delta t/\Delta v$	Input transition rise or fall rate	0	10	ns/V
$T_A$	Operating free-air temperature	-40	85	°C

NOTE 2: Unused or floating inputs must be held high or low.

### electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS	$V_{CC}^{\#}$	$T_A = -40^\circ\text{C}$ to $85^\circ\text{C}$		UNIT
			MIN	MAX	
$V_{OH}$	$I_{OH} = -100 \mu\text{A}$	$V_{CC} = 0.2$	2.7 V	2.2	V
	$I_{OH} = -12 \text{ mA}$				
	$I_{OH} = -24 \text{ mA}$				
$V_{OL}$	$I_{OL} = 100 \mu\text{A}$	$V_{CC} = 0.2$	3 V	2	V
	$I_{OL} = 12 \text{ mA}$				
	$I_{OL} = 24 \text{ mA}$				
$I_I$	$V_I = V_{CC}$ or GND	3.6 V	$\pm 5$	$\mu\text{A}$	
	$I_{CC}$				
	$I_{CC}$				
$\Delta I_{CC}$	$V_{CC} = 3$ V to 3.6 V, Other inputs at $V_{CC}$ or GND	One input at $V_{CC} - 0.6$ V, Other inputs at $V_{CC}$ or GND	3.6 V	500	$\mu\text{A}$
	$C_I$				
			3.3 V		pF

‡ For conditions shown as MIN or MAX, use the appropriate values under recommended operating conditions.

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