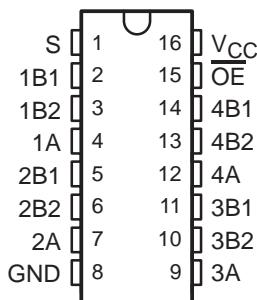


# SN74CBTLV3257 LOW-VOLTAGE 4-BIT 1-OF-2 FET MULTIPLEXER/DEMULITPLEXER

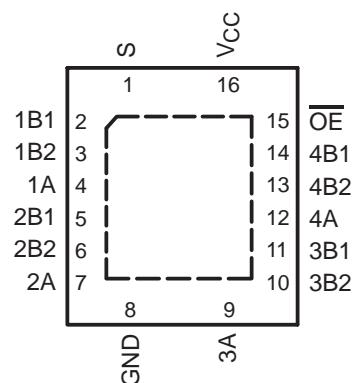
SCDS040I – DECEMBER 1997 – REVISED OCTOBER 2003

- 5- $\Omega$  Switch Connection Between Two Ports
- Rail-to-Rail Switching on Data I/O Ports
- $I_{off}$  Supports Partial-Power-Down Mode Operation
- Latch-Up Performance Exceeds 100 mA Per JESD 78, Class II
- ESD Protection Exceeds JESD 22
  - 2000-V Human-Body Model (A114-A)
  - 200-V Machine Model (A115-A)

D, DBQ, DGV, OR PW PACKAGE  
(TOP VIEW)



RGY PACKAGE  
(TOP VIEW)



## description/ordering information

The SN74CBTLV3257 is a 4-bit 1-of-2 high-speed FET multiplexer/demultiplexer. The low on-state resistance of the switch allows connections to be made with minimal propagation delay.

The select (S) input controls the data flow. The FET multiplexers/demultiplexers are disabled when the output-enable ( $\overline{OE}$ ) input is high.

This device is fully specified for partial-power-down applications using  $I_{off}$ . The  $I_{off}$  feature ensures that damaging current will not backflow through the device when it is powered down. The device has isolation during power off.

To ensure the high-impedance state during power up or power down,  $\overline{OE}$  should be tied to V<sub>CC</sub> through a pullup resistor; the minimum value of the resistor is determined by the current-sinking capability of the driver.

## ORDERING INFORMATION

T <sub>A</sub>	PACKAGE <sup>†</sup>		ORDERABLE PART NUMBER	TOP-SIDE MARKING
-40°C to 85°C	QFN – RGY	Tape and reel	SN74CBTLV3257RGYR	CL257
	SOIC – D	Tube	SN74CBTLV3257D	CBTLV3257
		Tape and reel	SN74CBTLV3257DR	
	SSOP (QSOP) – DBQ	Tape and reel	SN74CBTLV3257DBQR	CL257
	TSSOP – PW	Tape and reel	SN74CBTLV3257PWR	CL257
	TVSOP – DGV	Tape and reel	SN74CBTLV3257DGVR	CL257

<sup>†</sup> Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at [www.ti.com/sc/package](http://www.ti.com/sc/package).



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 **TEXAS  
INSTRUMENTS**

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# SN74CBTLV3257

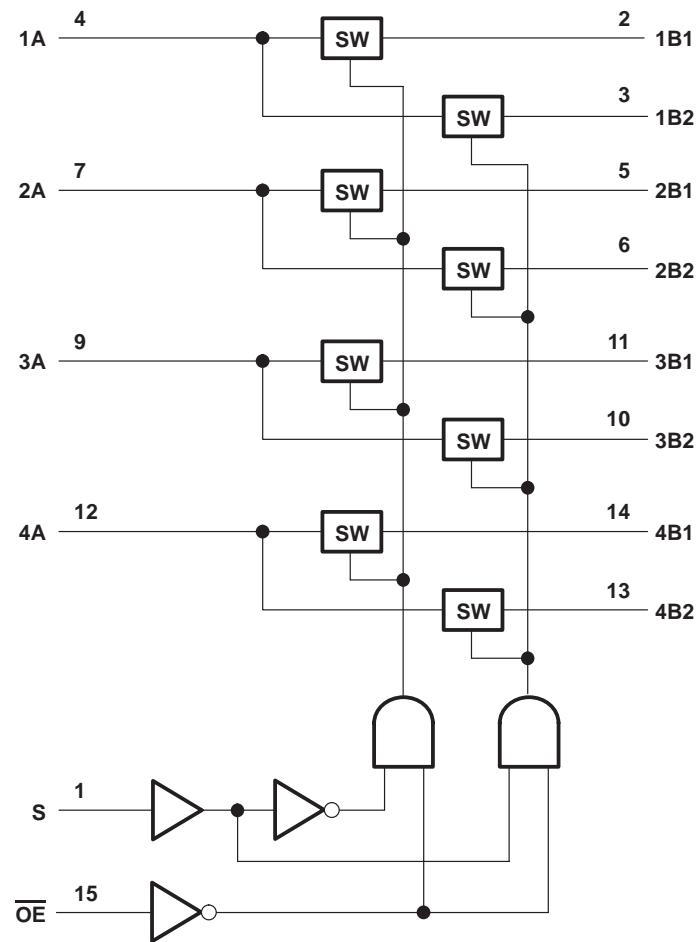
## LOW-VOLTAGE 4-BIT 1-OF-2 FET MULTIPLEXER/DEMULITPLEXER

SCDS040I – DECEMBER 1997 – REVISED OCTOBER 2003

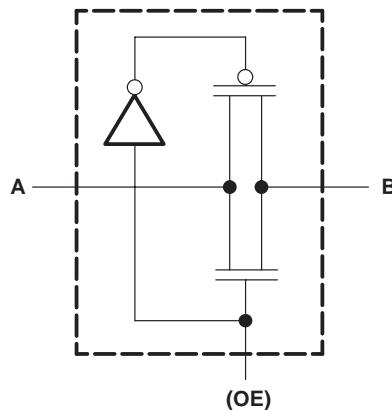
FUNCTION TABLE

INPUTS		FUNCTION
$\overline{OE}$	S	
L	L	A port = B1 port
L	H	A port = B2 port
H	X	Disconnect

logic diagram (positive logic)



**simplified schematic, each FET switch**



absolute maximum ratings over operating free-air temperature range (unless otherwise noted)<sup>†</sup>

<sup>†</sup> Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

- NOTES:** 1. The input and output negative-voltage ratings may be exceeded if the input and output clamp-current ratings are observed.  
 2. The package thermal impedance is calculated in accordance with JESD 51-7.  
 3. The package thermal impedance is calculated in accordance with JESD 51-5.

**recommended operating conditions (see Note 4)**

			MIN	MAX	UNIT
$V_{CC}$	Supply voltage		2.3	3.6	V
$V_{IH}$	High-level control input voltage	$V_{CC} = 2.3\text{ V to }2.7\text{ V}$	1.7		V
		$V_{CC} = 2.7\text{ V to }3.6\text{ V}$	2		
$V_{IL}$	Low-level control input voltage	$V_{CC} = 2.3\text{ V to }2.7\text{ V}$		0.7	V
		$V_{CC} = 2.7\text{ V to }3.6\text{ V}$		0.8	
$T_A$	Operating free-air temperature		-40	85	°C

**NOTE 4:** All unused control inputs of the device must be held at V<sub>CC</sub> or GND to ensure proper device operation. Refer to the TI application report, *Implications of Slow or Floating CMOS Inputs*, literature number SCBA004.

# SN74CBTLV3257

## LOW-VOLTAGE 4-BIT 1-OF-2 FET MULTIPLEXER/DEMULTIPLEXER

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**electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)**

PARAMETER		TEST CONDITIONS			MIN	TYP†	MAX	UNIT
V <sub>IK</sub>		V <sub>CC</sub> = 3 V,	I <sub>I</sub> = -18 mA			-1.2		V
I <sub>I</sub>		V <sub>CC</sub> = 3.6 V,	V <sub>I</sub> = V <sub>CC</sub> or GND			±1		µA
I <sub>off</sub>		V <sub>CC</sub> = 0,	V <sub>I</sub> or V <sub>O</sub> = 0 to 3.6 V			15		µA
I <sub>CC</sub>		V <sub>CC</sub> = 3.6 V,	I <sub>O</sub> = 0,	V <sub>I</sub> = V <sub>CC</sub> or GND		10		µA
ΔI <sub>CC</sub> ‡	Control inputs	V <sub>CC</sub> = 3.6 V,	One input at 3 V,	Other inputs at V <sub>CC</sub> or GND		300		µA
C <sub>i</sub>	Control inputs	V <sub>I</sub> = 3 V or 0				3		pF
C <sub>io(OFF)</sub>	A port	V <sub>O</sub> = 3 V or 0,			OE = V <sub>CC</sub>		10.5	pF
	B port	V <sub>O</sub> = 3 V or 0,			OE = V <sub>CC</sub>		5.5	
r <sub>on</sub> §	V <sub>CC</sub> = 2.3 V, TYP at V <sub>CC</sub> = 2.5 V	V <sub>I</sub> = 0	I <sub>I</sub> = 64 mA			5	8	Ω
			I <sub>I</sub> = 24 mA			5	8	
		V <sub>I</sub> = 1.7 V,	I <sub>I</sub> = 15 mA			27	40	
			I <sub>I</sub> = 64 mA			5	7	
	V <sub>CC</sub> = 3 V	V <sub>I</sub> = 0	I <sub>I</sub> = 24 mA			5	7	
			I <sub>I</sub> = 15 mA			10	15	

† All typical values are at V<sub>CC</sub> = 3.3 V (unless otherwise noted), T<sub>A</sub> = 25°C.

‡ This is the increase in supply current for each input that is at the specified voltage level, rather than V<sub>CC</sub> or GND.

§ Measured by the voltage drop between the A and the B terminals at the indicated current through the switch. On-state resistance is determined by the lower of the voltages of the two (A or B) terminals.

**switching characteristics over recommended operating free-air temperature range (unless otherwise noted) (see Figure 1)**

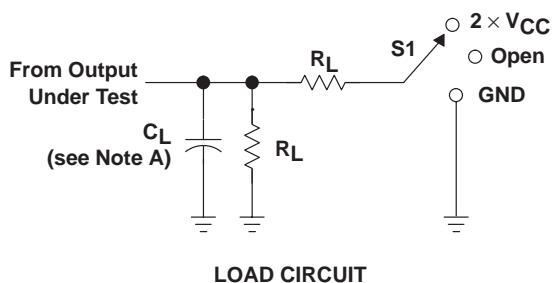
PARAMETER	FROM (INPUT)	TO (OUTPUT)	V <sub>CC</sub> = 2.5 V ± 0.2 V		V <sub>CC</sub> = 3.3 V ± 0.3 V		UNIT
			MIN	MAX	MIN	MAX	
t <sub>pd</sub>	A or B†	B or A	0.15		0.25		ns
	S	A or B	1.8	6.1	1.8	5.3	
t <sub>en</sub>	S	A or B	1.7	6.1	1.7	5.3	ns
t <sub>dis</sub>	S	A or B	1	4.8	1	4.5	ns
t <sub>en</sub>	OE	A or B	1.9	5.6	2	5	ns
t <sub>dis</sub>	OE	A or B	1	5.5	1.6	5.5	ns

† The propagation delay is the calculated RC time constant of the typical on-state resistance of the switch and the specified load capacitance, when driven by an ideal voltage source (zero output impedance).

SN74CBTLV3257  
LOW-VOLTAGE 4-BIT 1-OF-2 FET MULTIPLEXER/DEMULTIPLEXER

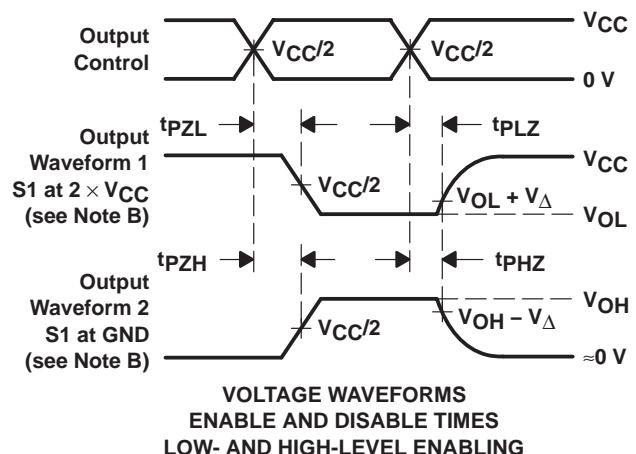
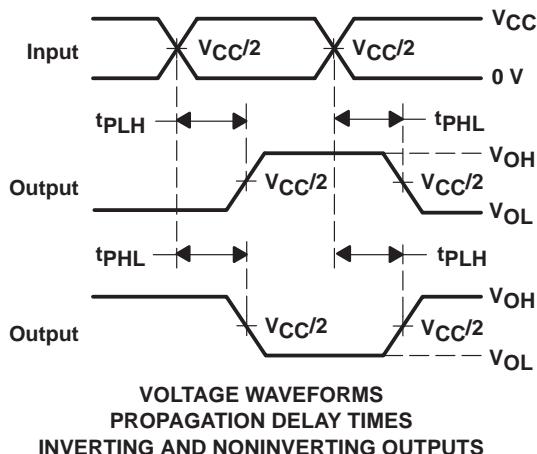
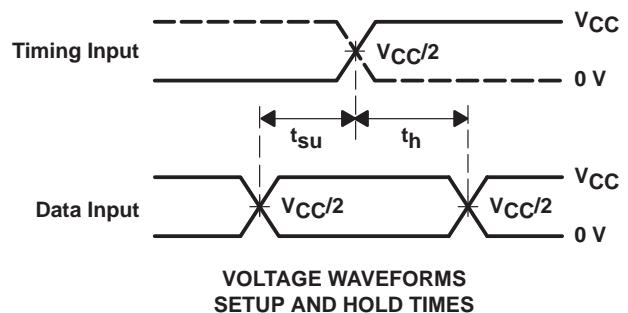
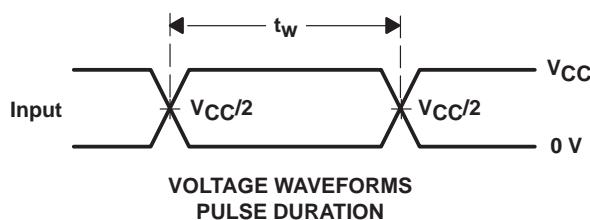
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**PARAMETER MEASUREMENT INFORMATION**



TEST	S1
$t_{PLH}/t_{PHL}$	Open
$t_{PLZ}/t_{PZL}$	$2 \times V_{CC}$
$t_{PHZ}/t_{PZH}$	GND

$V_{CC}$	$C_L$	$R_L$	$V_\Delta$
$2.5 V \pm 0.2 V$	30 pF	500 $\Omega$	0.15 V
$3.3 V \pm 0.3 V$	50 pF	500 $\Omega$	0.3 V



- NOTES:
- A.  $C_L$  includes probe and jig capacitance.
  - B. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
  - C. All input pulses are supplied by generators having the following characteristics: PRR  $\leq 10$  MHz,  $Z_O = 50 \Omega$ ,  $t_r \leq 2$  ns,  $t_f \leq 2$  ns.
  - D. The outputs are measured one at a time with one transition per measurement.
  - E.  $t_{PLZ}$  and  $t_{PHZ}$  are the same as  $t_{dis}$ .
  - F.  $t_{PZL}$  and  $t_{PZH}$  are the same as  $t_{en}$ .
  - G.  $t_{PLH}$  and  $t_{PHL}$  are the same as  $t_{pd}$ .
  - H. All parameters and waveforms are not applicable to all devices.

**Figure 1. Load Circuit and Voltage Waveforms**

**PACKAGING INFORMATION**

Orderable Device	Status <sup>(1)</sup>	Package Type	Package Drawing	Pins	Package Qty	Eco Plan <sup>(2)</sup>	Lead/Ball Finish	MSL Peak Temp <sup>(3)</sup>
74CBTLV3257DBQRE4	ACTIVE	SSOP/ QSOP	DBQ	16	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1YEAR
74CBTLV3257DGVRE4	ACTIVE	TVSOP	DGV	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
74CBTLV3257PWRE4	ACTIVE	TSSOP	PW	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
74CBTLV3257PWRG4	ACTIVE	TSSOP	PW	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
74CBTLV3257RGYRG4	ACTIVE	QFN	RGY	16	1000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1YEAR
SN74CBTLV3257D	ACTIVE	SOIC	D	16	40	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74CBTLV3257DBQR	ACTIVE	SSOP/ QSOP	DBQ	16	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1YEAR
SN74CBTLV3257DE4	ACTIVE	SOIC	D	16	40	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74CBTLV3257DGVR	ACTIVE	TVSOP	DGV	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74CBTLV3257DR	ACTIVE	SOIC	D	16	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74CBTLV3257DRE4	ACTIVE	SOIC	D	16	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74CBTLV3257PW	ACTIVE	TSSOP	PW	16	90	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74CBTLV3257PWG4	ACTIVE	TSSOP	PW	16	90	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74CBTLV3257PWR	ACTIVE	TSSOP	PW	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74CBTLV3257RGYR	ACTIVE	QFN	RGY	16	1000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1YEAR

<sup>(1)</sup> The marketing status values are defined as follows:

**ACTIVE:** Product device recommended for new designs.

**LIFEBUY:** TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

**NRND:** Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

<sup>(2)</sup> Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS) or Green (RoHS & no Sb/Br) - please check <http://www.ti.com/productcontent> for the latest availability information and additional product content details.

**TBD:** The Pb-Free/Green conversion plan has not been defined.

**Pb-Free (RoHS):** TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

**Green (RoHS & no Sb/Br):** TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

<sup>(3)</sup> MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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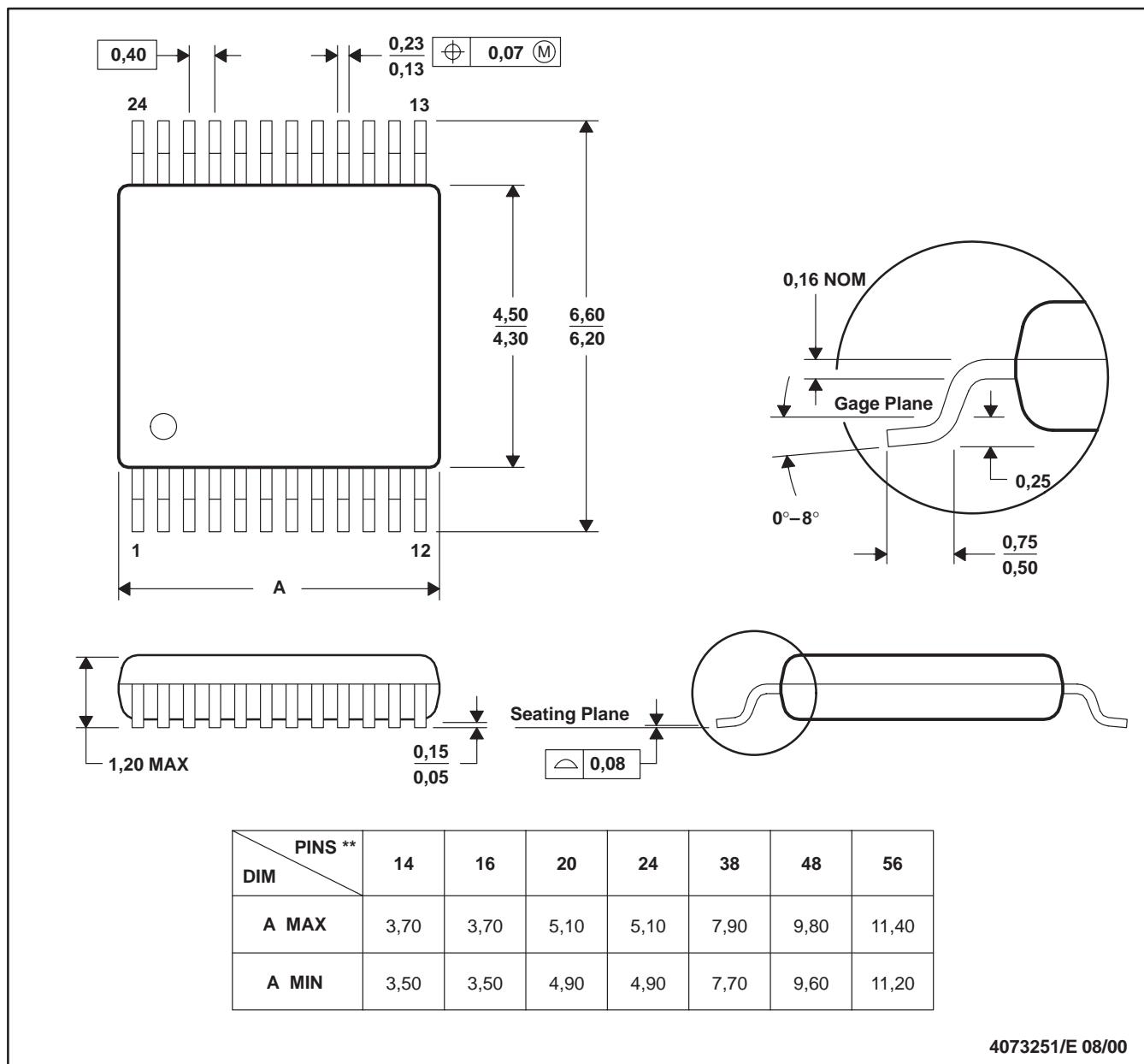
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## DGV (R-PDSO-G\*\*)

## PLASTIC SMALL-OUTLINE

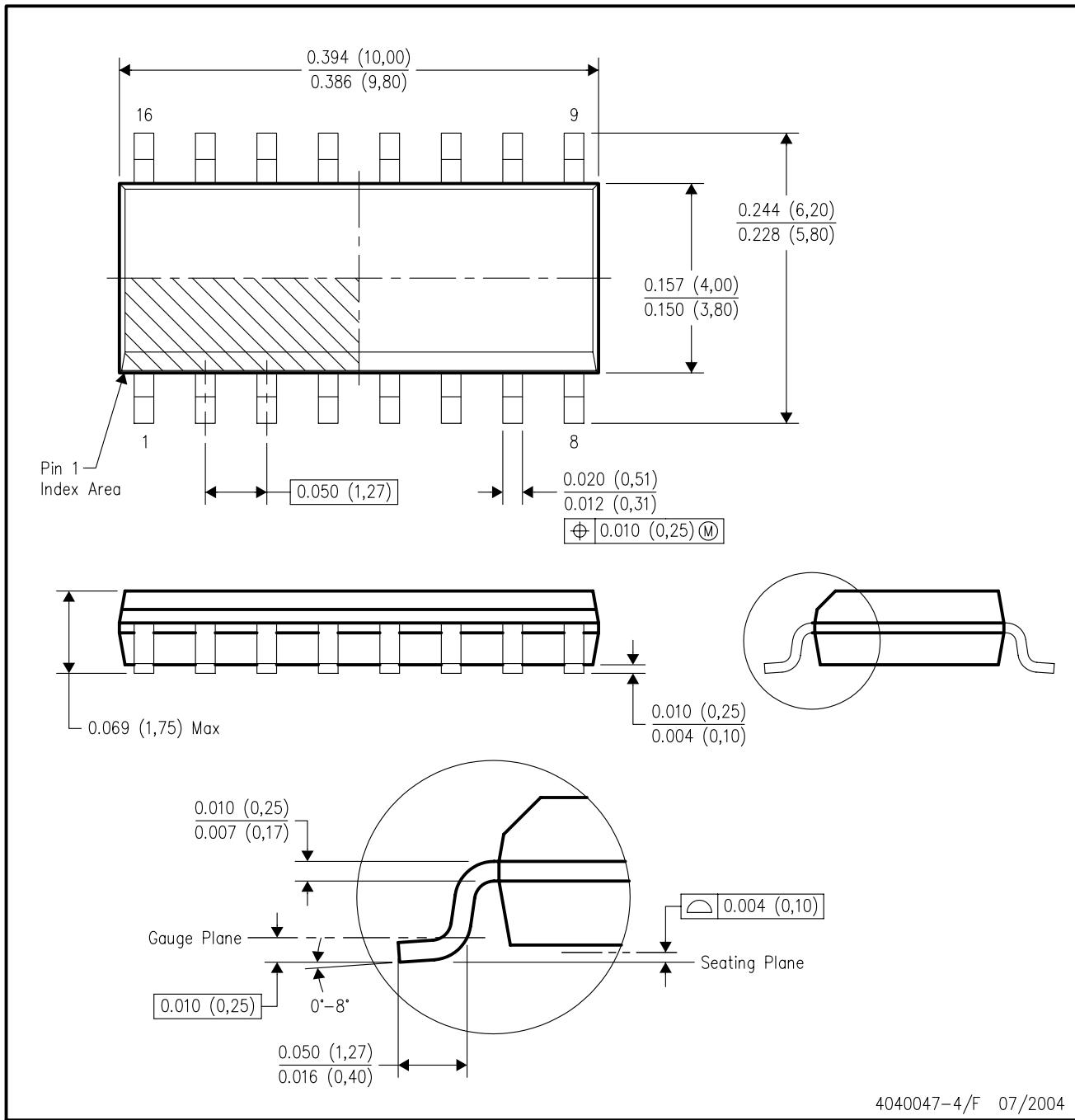
24 PINS SHOWN



- NOTES:
- A. All linear dimensions are in millimeters.
  - B. This drawing is subject to change without notice.
  - C. Body dimensions do not include mold flash or protrusion, not to exceed 0,15 per side.
  - D. Falls within JEDEC: 24/48 Pins – MO-153  
14/16/20/56 Pins – MO-194

## D (R-PDSO-G16)

## PLASTIC SMALL-OUTLINE PACKAGE



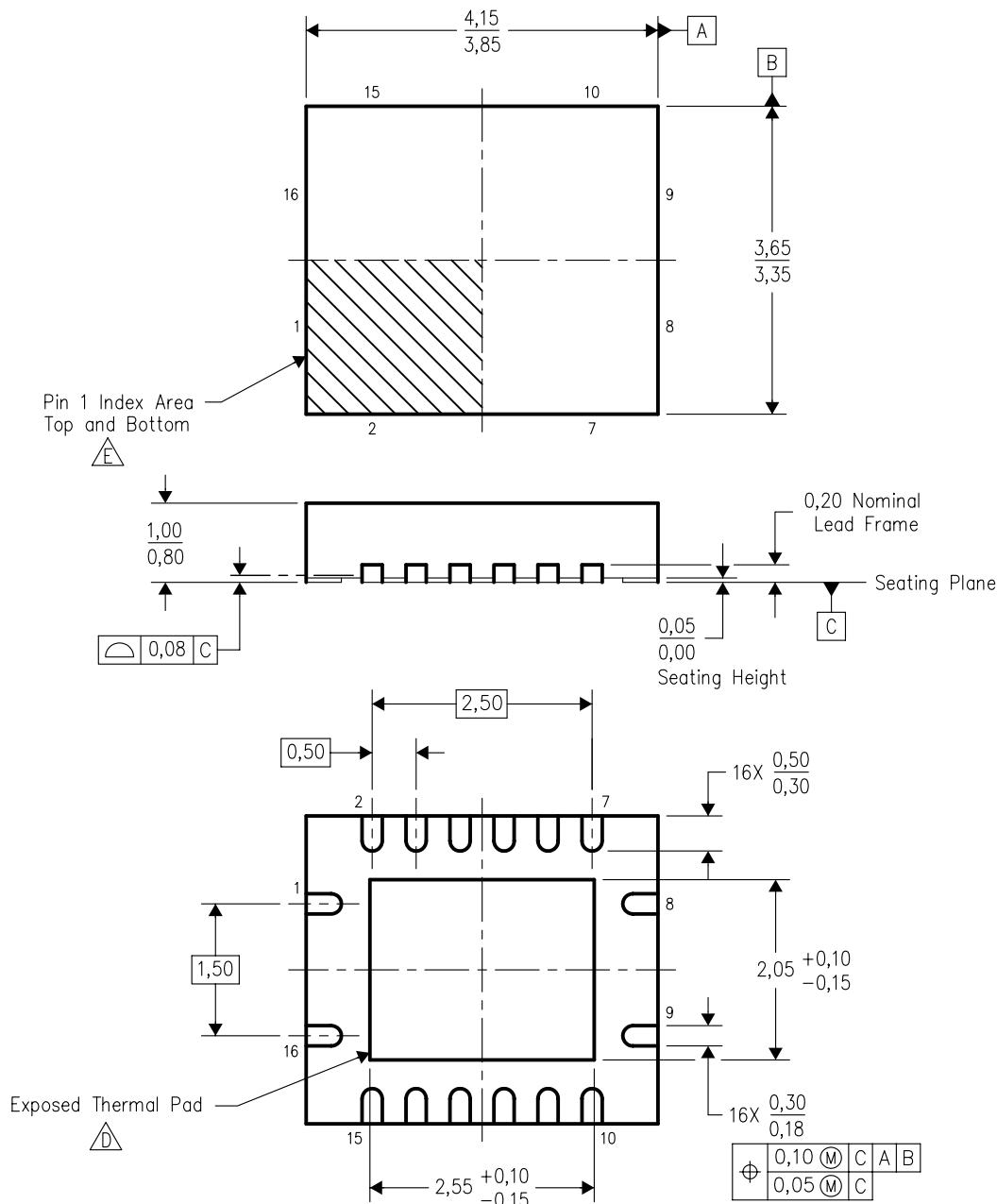
4040047-4/F 07/2004

- NOTES:
- All linear dimensions are in inches (millimeters).
  - This drawing is subject to change without notice.
  - Body dimensions do not include mold flash or protrusion not to exceed 0.006 (0,15).
  - Falls within JEDEC MS-012 variation AC.

## MECHANICAL DATA

**RGY (R-PQFP-N16)**

**PLASTIC QUAD FLATPACK**



Bottom View

4203539-3/G 04/2005

NOTES: A. All linear dimensions are in millimeters. Dimensioning and tolerancing per ASME Y14.5M-1994.

B. This drawing is subject to change without notice.

C. QFN (Quad Flatpack No-Lead) package configuration.

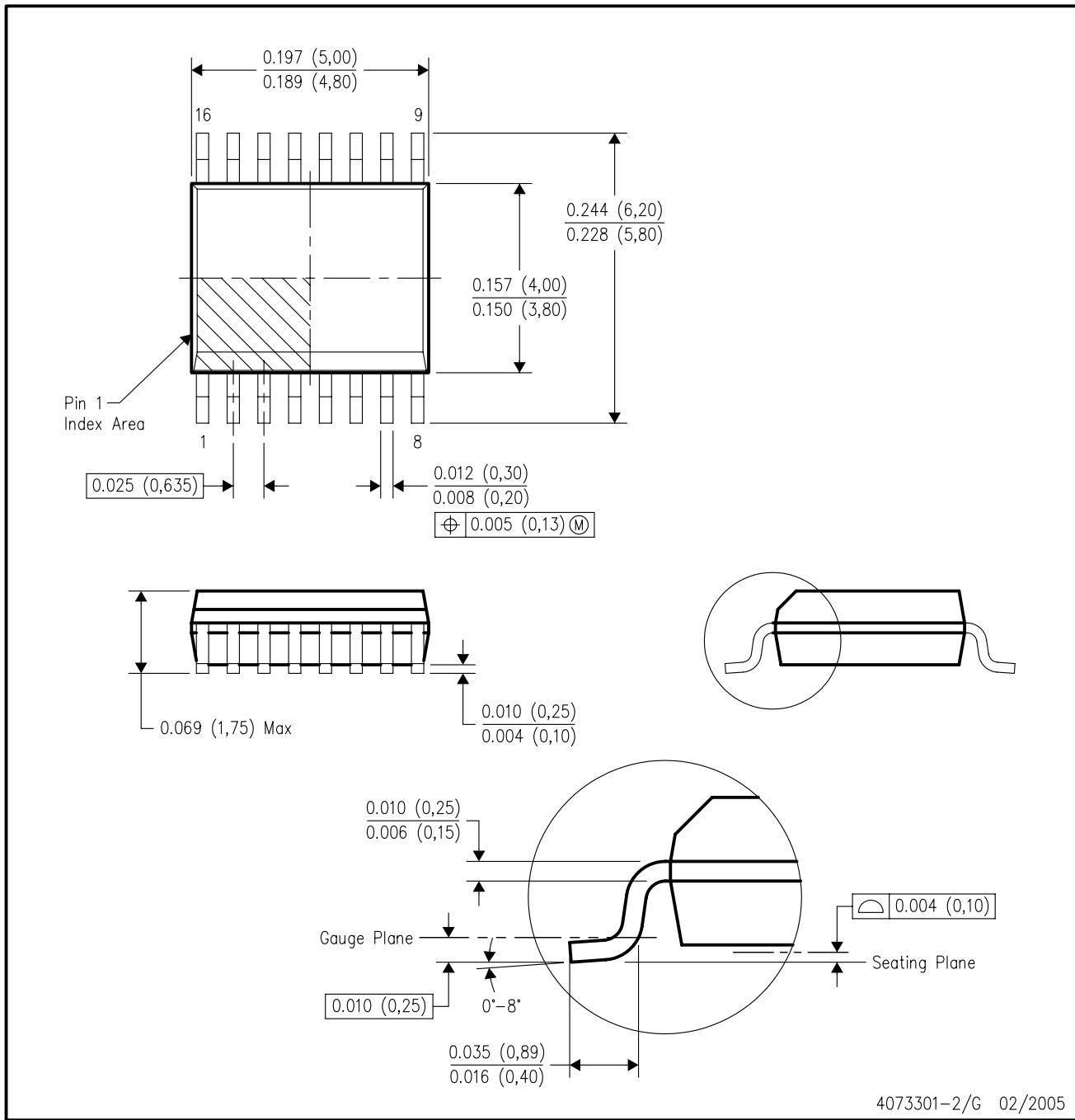
D. The package thermal pad must be soldered to the board for thermal and mechanical performance.

E. Pin 1 identifiers are located on both top and bottom of the package and within the zone indicated.  
The Pin 1 identifiers are either a molded, marked, or metal feature.

F. Package complies to JEDEC MO-241 variation BB.

## DBQ (R-PDSO-G16)

## PLASTIC SMALL-OUTLINE PACKAGE

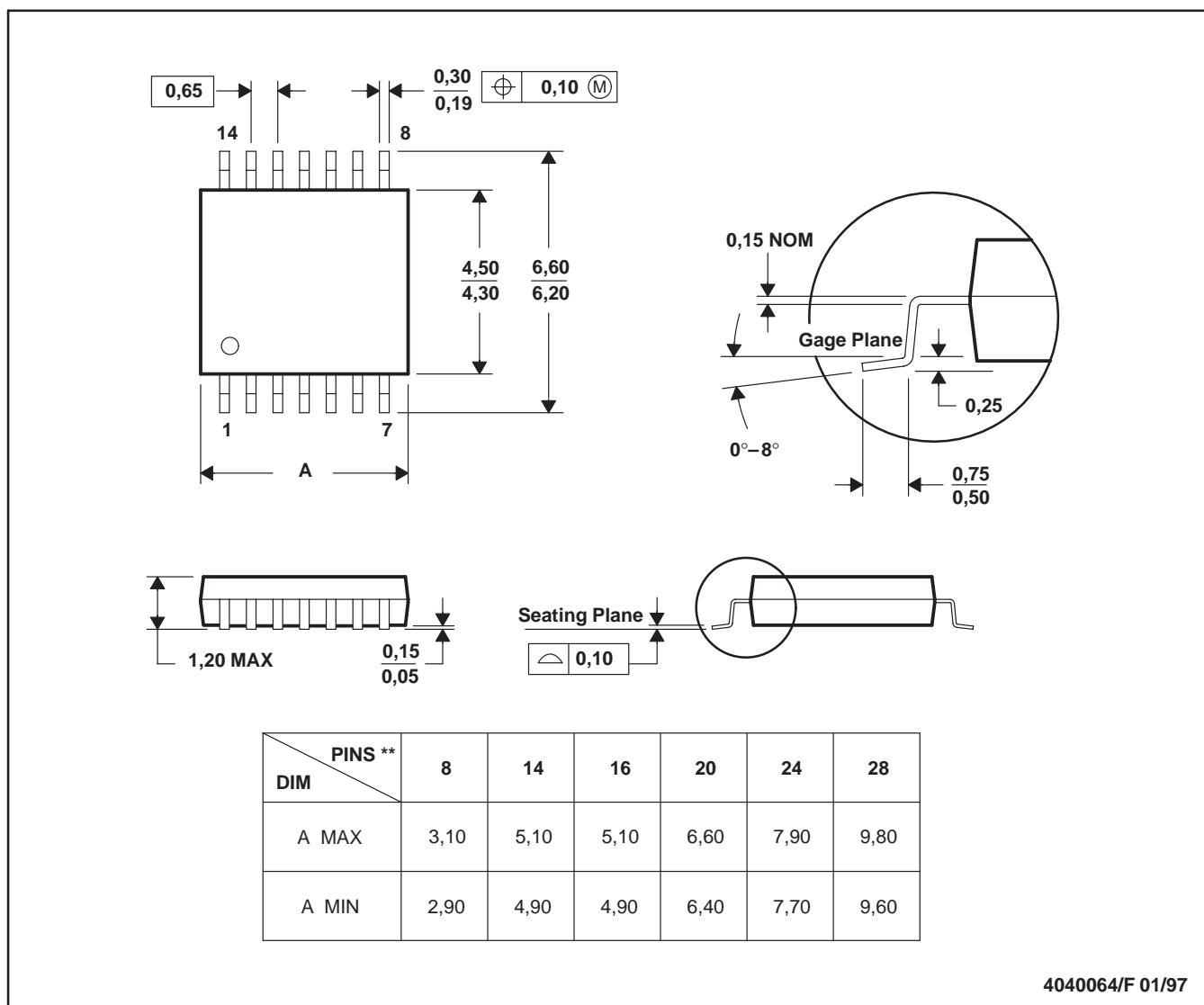


- NOTES:
- All linear dimensions are in inches (millimeters).
  - This drawing is subject to change without notice.
  - Body dimensions do not include mold flash or protrusion not to exceed 0.006 (0,15) per side.
  - Falls within JEDEC MO-137 variation AB.

## PW (R-PDSO-G\*\*)

## PLASTIC SMALL-OUTLINE PACKAGE

14 PINS SHOWN



- NOTES:
- All linear dimensions are in millimeters.
  - This drawing is subject to change without notice.
  - Body dimensions do not include mold flash or protrusion not to exceed 0,15.
  - Falls within JEDEC MO-153

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View ROHS Compliant Devices

View RoHS Compliant Devices

 clear gif**SN74CBTLV3257, Status: ACTIVE****Low-Voltage 4-Bit 1-Of-2 FET Multiplexer/Demultiplexer**

### Pricing/Packaging/CAD Design Tools/Samples

<input type="checkbox"/>				Price	Packaging			CAD Design Tools	Samples
Device	Status	Temp (°C)	Budget Price (\$US)   QTY	Industry Standard (TI Pkg)   Pins	Top Side Marking	Standard Pack Quantity	Footprints		Samples
74CBTLV3257DBQRE4	ACTIVE	-40 to 85	0.48   1KU	SSOP/QSOP (DBQ)   16	<a href="#">View</a>	2500	<input type="checkbox"/>		Request Free Samples
74CBTLV3257DGVRE4	ACTIVE	-40 to 85	0.48   1KU	TVSOP (DGV)   16		2000	<input type="checkbox"/>		Request Free Samples
74CBTLV3257PWRE4	ACTIVE	-40 to 85	0.48   1KU	TSSOP (PW)   16	<a href="#">View</a>	2000	<input type="checkbox"/>		Request Free Samples
74CBTLV3257PWRG4	ACTIVE	-40 to 85	0.55   1KU	TSSOP (PW)   16	<a href="#">View</a>	2000	<input type="checkbox"/>		Purchase Samples
74CBTLV3257RGYRG4	ACTIVE	-40 to 85	0.66   1KU	QFN (RGY)   16		1000			Request Free Samples
SN74CBTLV3257D	ACTIVE	-40 to 85	0.48   1KU	SOIC (D)   16	<a href="#">View</a>	40	<input type="checkbox"/>		Purchase Samples
SN74CBTLV3257DBQR	ACTIVE	-40 to 85	0.48   1KU	SSOP/QSOP (DBQ)   16	<a href="#">View</a>	2500	<input type="checkbox"/>		Contact TI Distributor or Sales Office
SN74CBTLV3257DE4	ACTIVE	-40 to 85	0.48   1KU	SOIC (D)   16	<a href="#">View</a>	40	<input type="checkbox"/>		Purchase Samples
SN74CBTLV3257DGVR	ACTIVE	-40 to 85	0.48   1KU	TVSOP (DGV)   16		2000	<input type="checkbox"/>		Contact TI Distributor or Sales Office
SN74CBTLV3257DR	ACTIVE	-40 to 85	0.48   1KU	SOIC (D)   16	<a href="#">View</a>	2500	<input type="checkbox"/>		Contact TI Distributor or Sales Office
SN74CBTLV3257DRE4	ACTIVE	-40 to 85	0.48   1KU	SOIC (D)   16	<a href="#">View</a>	2500	<input type="checkbox"/>		Request Free Samples
SN74CBTLV3257PW	ACTIVE	-40 to 85	0.48   1KU	TSSOP (PW)   16	<a href="#">View</a>	90	<input type="checkbox"/>		Purchase Samples
SN74CBTLV3257PWG4	ACTIVE	-40 to 85	0.55   1KU	TSSOP (PW)   16	<a href="#">View</a>	90	<input type="checkbox"/>		Purchase Samples
SN74CBTLV3257PWR	ACTIVE	-40 to 85	0.48   1KU	TSSOP (PW)   16	<a href="#">View</a>	2000	<input type="checkbox"/>		Contact TI Distributor or Sales Office
SN74CBTLV3257RGYR	ACTIVE	-40 to 85	0.59   1KU	QFN (RGY)   16		1000			Request Free Samples

### Inventory

<input type="checkbox"/>	TI Inventory Status			Reported Distributor Inventory			
74CBTLV3257DBQRE4	As of 9:08 AM GMT, 25 Nov 2005			As of 9:08 AM GMT, 25 Nov 2005			
	In Stock	In Progress QTY   Date	Lead Time	Region	Company	In Stock	Purchase
	0*	>10k   16 Dec	4 Weeks	None Reported	<a href="#">View Distributors</a>		
74CBTLV3257DGVRE4	As of 9:08 AM GMT, 25 Nov 2005			As of 9:08 AM GMT, 25 Nov 2005			
	In Stock	In Progress QTY   Date	Lead Time	Region	Company	In Stock	Purchase
	0*	1213   8 Dec	10 Weeks	None Reported	<a href="#">View Distributors</a>		
		777   30 Dec					
		678   3 Feb					
		48   10 Feb					
		833   24 Feb					
74CBTLV3257PWRE4	As of 9:08 AM GMT, 25 Nov 2005			As of 9:08 AM GMT, 25 Nov 2005			

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[Choose a Region](#)



	In Stock	In Progress QTY   Date	Lead Time	Region	Company	In Stock	Purchase
	0*	>10k   31 Mar	16 Weeks	None Reported	<a href="#">View Distributors</a>		
74CBTLV3257PWRG4	As of 9:08 AM GMT, 25 Nov 2005				As of 9:08 AM GMT, 25 Nov 2005		
	In Stock	In Progress QTY   Date	Lead Time	Region	Company	In Stock	Purchase
	0*		16 Weeks	None Reported	<a href="#">View Distributors</a>		
74CBTLV3257RGYRG4	As of 9:08 AM GMT, 25 Nov 2005				As of 9:08 AM GMT, 25 Nov 2005		
	In Stock	In Progress QTY   Date	Lead Time	Region	Company	In Stock	Purchase
	0*	>10k   11 Jan	7 Weeks	None Reported	<a href="#">View Distributors</a>		
SN74CBTLV3257D	As of 9:08 AM GMT, 25 Nov 2005				As of 9:08 AM GMT, 25 Nov 2005		
	In Stock	In Progress QTY   Date	Lead Time	Region	Company	In Stock	Purchase
	>10k*	>10k   18 Jan	10 Weeks	Americas	Avnet	115	
					DigiKey	490	
				Europe	Avnet-SILICA	640	
					EBV Elektronik	40	
					Spoerle	230	
SN74CBTLV3257DBQR	As of 9:08 AM GMT, 25 Nov 2005				As of 9:08 AM GMT, 25 Nov 2005		
	In Stock	In Progress QTY   Date	Lead Time	Region	Company	In Stock	Purchase
	0*	>10k   16 Dec	4 Weeks	Americas	DigiKey	608	
SN74CBTLV3257DE4	As of 9:08 AM GMT, 25 Nov 2005				As of 9:08 AM GMT, 25 Nov 2005		
	In Stock	In Progress QTY   Date	Lead Time	Region	Company	In Stock	Purchase
	>10k*	>10k   18 Jan	10 Weeks	None Reported	<a href="#">View Distributors</a>		
SN74CBTLV3257DGVR	As of 9:08 AM GMT, 25 Nov 2005				As of 9:08 AM GMT, 25 Nov 2005		
	In Stock	In Progress QTY   Date	Lead Time	Region	Company	In Stock	Purchase
	0*	1213   8 Dec	10 Weeks	Americas	DigiKey	>1k	
		777   30 Dec					
		678   3 Feb					
		48   10 Feb					
		833   24 Feb					
SN74CBTLV3257DR	As of 9:08 AM GMT, 25 Nov 2005				As of 9:08 AM GMT, 25 Nov 2005		
	In Stock	In Progress QTY   Date	Lead Time	Region	Company	In Stock	Purchase
	0*	>10k   16 Jan	10 Weeks	Americas	Avnet	>1k	
					DigiKey	762	
SN74CBTLV3257DRE4	As of 9:08 AM GMT, 25 Nov 2005				As of 9:08 AM GMT, 25 Nov 2005		
	In Stock	In Progress QTY   Date	Lead Time	Region	Company	In Stock	Purchase
	0*	>10k   16 Jan	10 Weeks	None Reported	<a href="#">View Distributors</a>		

<b>SN74CBTLV3257PW</b>	As of 9:08 AM GMT, 25 Nov 2005			As of 9:08 AM GMT, 25 Nov 2005			
	In Stock	In Progress QTY   Date	Lead Time	Region	Company	In Stock	Purchase
	0*	>10k   31 Mar	16 Weeks	Europe	Rutronik	260	
<b>SN74CBTLV3257PWG4</b>	As of 9:08 AM GMT, 25 Nov 2005			As of 9:08 AM GMT, 25 Nov 2005			
	In Stock	In Progress QTY   Date	Lead Time	Region	Company	In Stock	Purchase
	0*	990   8 Dec	12 Weeks	None Reported	<a href="#">View Distributors</a>		
<b>SN74CBTLV3257PWR</b>	As of 9:08 AM GMT, 25 Nov 2005			As of 9:08 AM GMT, 25 Nov 2005			
	In Stock	In Progress QTY   Date	Lead Time	Region	Company	In Stock	Purchase
	0*	>10k   31 Mar	16 Weeks	Americas	<a href="#">Avnet</a>	>1k	
<b>SN74CBTLV3257RGYR</b>	As of 9:08 AM GMT, 25 Nov 2005			As of 9:08 AM GMT, 25 Nov 2005			
	In Stock	In Progress QTY   Date	Lead Time	Region	Company	In Stock	Purchase
	0*	>10k   11 Jan	7 Weeks	None Reported	<a href="#">View Distributors</a>		

\* Our information is updated daily, so please check back with us soon if this does not meet your needs. You may also contact your [TI Authorized Distributor](#), including those [listed above](#), for real time stock information.

\*\* Lead time information is not available at this time. However, our information is updated daily so please check back with us soon. Please contact your preferred [TI Authorized Distributor](#) for additional information.

### Quality & Lead (Pb)-Free Data

<input type="checkbox"/>	Product Content					MTBF/FIT Rate
Device	Eco Plan*	Lead/Ball Finish	MSL Rating/Peak Reflow	Details	Details	
74CBTLV3257DBQRE4 <input type="checkbox"/>	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1YEAR	<a href="#">View</a>	<a href="#">View</a>	
74CBTLV3257DGVRE4 <input type="checkbox"/>	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	<a href="#">View</a>	<a href="#">View</a>	
74CBTLV3257PWRE4 <input type="checkbox"/>	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	<a href="#">View</a>	<a href="#">View</a>	
74CBTLV3257PWRG4 <input type="checkbox"/>	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	<a href="#">View</a>	<a href="#">View</a>	
74CBTLV3257RGYRG4 <input type="checkbox"/>	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1YEAR	<a href="#">View</a>	<a href="#">View</a>	
SN74CBTLV3257D <input type="checkbox"/>	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	<a href="#">View</a>	<a href="#">View</a>	
SN74CBTLV3257DBQR <input type="checkbox"/>	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1YEAR	<a href="#">View</a>	<a href="#">View</a>	
SN74CBTLV3257DE4 <input type="checkbox"/>	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	<a href="#">View</a>	<a href="#">View</a>	
SN74CBTLV3257DGVR <input type="checkbox"/>	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	<a href="#">View</a>	<a href="#">View</a>	
SN74CBTLV3257DR <input type="checkbox"/>	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	<a href="#">View</a>	<a href="#">View</a>	
SN74CBTLV3257DRE4 <input type="checkbox"/>	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	<a href="#">View</a>	<a href="#">View</a>	
SN74CBTLV3257PW <input type="checkbox"/>	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	<a href="#">View</a>	<a href="#">View</a>	
SN74CBTLV3257PWG4 <input type="checkbox"/>	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	<a href="#">View</a>	<a href="#">View</a>	
SN74CBTLV3257PWR <input type="checkbox"/>	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	<a href="#">View</a>	<a href="#">View</a>	
SN74CBTLV3257RGYR <input type="checkbox"/>	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1YEAR	<a href="#">View</a>	<a href="#">View</a>	

\* The planned eco-friendly classification: Pb-Free (RoHS) or Green (RoHS & no Sb/Br) - please click on the Product Content Details "View" link in the table above for the latest availability information and additional product content details.

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### Technical Documents

<input type="checkbox"/> Datasheets	Keep track of what's new <input type="checkbox"/>
<b>SN74CBTLV3257 (Rev. I)</b> (sn74cbtlv3257.pdf, 378 KB)	
10 Oct 2003 <a href="#">Download</a>	

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