

Complementary Silicon Plastic Power Transistors

... designed for use in general purpose amplifier and switching applications.

• Collector – Emitter Saturation Voltage —

 $V_{CE(sat)} = 1.5 \text{ Vdc (Max)} @ I_C = 6.0 \text{ Adc}$

• Collector Emitter Sustaining Voltage —

V_{CEO(sus)} = 80 Vdc (Min) — BD243B, BD244B = 100 Vdc (Min) — BD243C, BD244C

• High Current Gain Bandwidth Product

 $f_T = 3.0 \text{ MHz (Min)} @ I_C = 500 \text{ mAdc}$

• Compact TO-220 AB Package

MAXIMUM RATINGS

Rating	Symbol	BD243B BD244B	BD243C BD244C	Unit		
Collector–Emitter Voltage	V _{CEO}	80	100	Vdc		
Collector-Base Voltage	V _{CB}	80	100	Vdc		
Emitter-Base Voltage	V _{EB}	5.0		Vdc		
Collector Current — Continuous Peak	lc	6		Adc		
Base Current	I _B	2.0		I _B 2.0		Adc
Total Device Dissipation @ T _C = 25°C Derate above 25°C	P _D	65 0.52		Watts W/°C		
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-65 to +150		°C		

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	$R_{ heta JC}$	1.92	°C/W

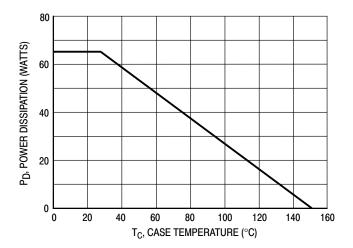


Figure 1. Power Derating

BD243B
BD243C*
PNP
BD244B
BD244C*

*ON Semiconductor Preferred Device

6 AMPERE
POWER TRANSISTORS
COMPLEMENTARY
SILICON
80-100 VOLTS
65 WATTS

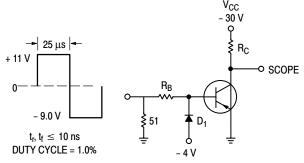


ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Characte	eristic	Symbol	Min	Max	Unit
Collector–Emitter Sustaining Voltage (1) (I _C = 30 mAdc, I _B = 0)	BD243B, BD244B BD243C, BD244C	V _{CEO(sus)}	80 100		Vdc
Collector Cutoff Current (V _{CE} = 60 Vdc, I _B = 0)	BD243B, BD243C, BD244B, BD244C	I _{CEO}	_	0.7	mAdc
Collector Cutoff Current (V _{CE} = 80 Vdc, V _{EB} = 0) (V _{CE} = 100 Vdc, V _{EB} = 0)	BD243B, BD244B BD243C, BD244C	I _{CES}		400 400	μAdc
Emitter Cutoff Current (V _{BE} = 5.0 Vdc, I _C = 0)		I _{EBO}	_	1.0	mAdc
ON CHARACTERISTICS (1)					
DC Current Gain $ (I_C = 0.3 \text{ Adc}, V_{CE} = 4.0 \text{ Vdc}) $ $ (I_C = 3.0 \text{ Adc}, V_{CE} = 4.0 \text{ Vdc}) $		h _{FE}	30 15		_
Collector–Emitter Saturation Voltage (I _C = 6.0 Adc, I _B = 1.0 Adc)		V _{CE(sat)}	_	1.5	Vdc
Base–Emitter On Voltage (I _C = 6.0 Adc, V _{CE} = 4.0 Vdc)		V _{BE(on)}	_	2.0	Vdc
DYNAMIC CHARACTERISTICS					
Current–Gain — Bandwidth Product (2) (Ic = 500 mAdc, VcE = 10 Vdc, ftest = 1.0	MHz)	f _T	3.0	_	MHz

Current–Gain — Bandwidth Product (2) $(I_C = 500 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}, f_{test} = 1.0 \text{ MHz})$	^T Τ	3.0	_	IVIHZ
Small–Signal Current Gain (I _C = 0.5 Adc, V _{CE} = 10 Vdc, f = 1.0 kHz)	h _{fe}	20	_	_

⁽¹⁾ Pulse Test: Pulsewidth \leq 300 μ s, Duty Cycle \leq 2.0%.



 R_B and R_C varied to obtain desired current levels D_1 must be fast recovery type eg. 1N5825 used above I $_B\approx 100$ ma MSD6100 used below I $_B\approx 100$ ma

Figure 2. Switching Time Test Circuit

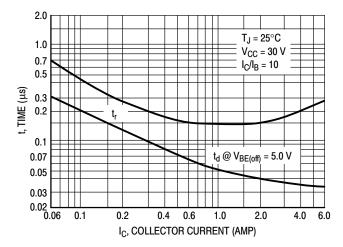


Figure 3. Turn-On Time

⁽²⁾ $f_T = h_{fe} \bullet f_{test}$

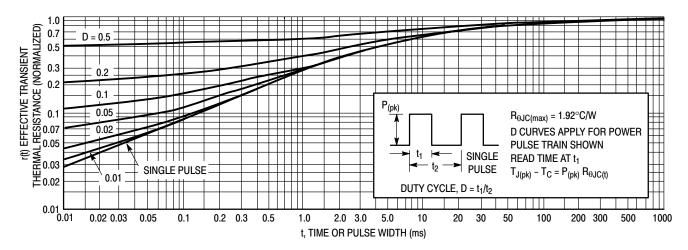


Figure 4. Thermal Response

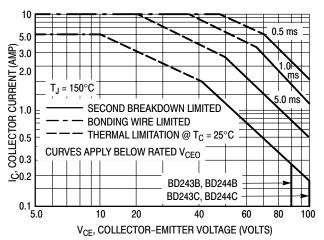
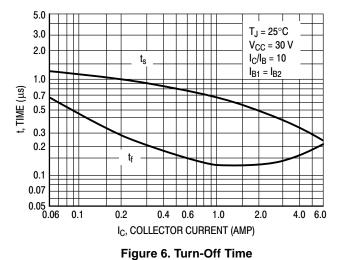


Figure 5. Active Region Safe Operating Area

There are two limitations on the power handling ability of a transistor: average junction temperature and second breakdown. Safe operating area curves indicate $I_C - V_{CE}$ limits of the transistor that must be observed for reliable operation, i.e., the transistor must not be subjected to greater dissipation than the curves indicate.

The data of Figure 5 is based on $T_{J(pk)} = 150^{\circ}C$: T_{C} is variable depending on conditions. Second breakdown pulse limits are valid for duty cycles to 10% provided $T_{J(pk)} \le 150^{\circ}C$, $T_{J(pk)}$ may be calculated from the data in Figure 4. At high case temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by second breakdown.



300 $T_J = 25^{\circ}C$ 200 CAPACITANCE (pF) C_{ib} 100 70 50 30 **└** 0.5 1.0 2.0 3.0 5.0 20 30 50 V_R, REVERSE VOLTAGE (VOLTS)

Figure 7. Capacitance

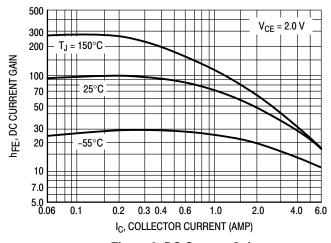


Figure 8. DC Current Gain

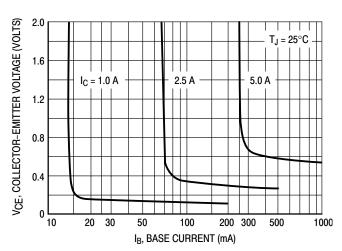


Figure 9. Collector Saturation Region

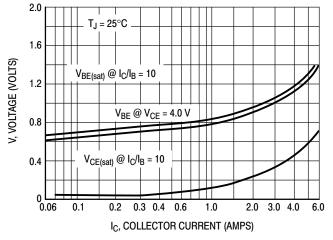


Figure 10. "On" Voltages

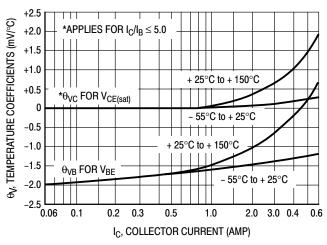


Figure 11. Temperature Coefficients

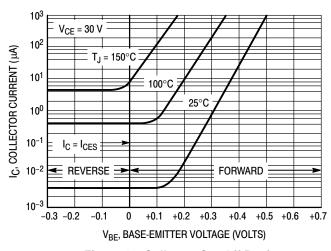


Figure 12. Collector Cut-Off Region

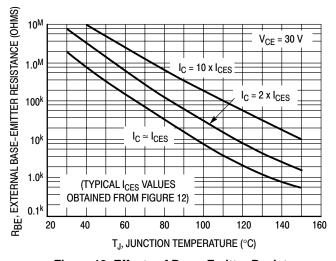
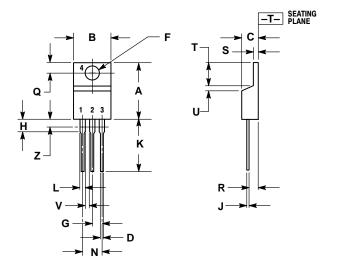


Figure 13. Effects of Base-Emitter Resistance

PACKAGE DIMENSIONS

TO-220 CASE 221A-09 ISSUE AA



- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED.

	INC	INCHES MILLIMETERS		IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.570	0.620	14.48	15.75
В	0.380	0.405	9.66	10.28
С	0.160	0.190	4.07	4.82
D	0.025	0.035	0.64	0.88
F	0.142	0.147	3.61	3.73
G	0.095	0.105	2.42	2.66
Н	0.110	0.155	2.80	3.93
J	0.018	0.025	0.46	0.64
K	0.500	0.562	12.70	14.27
L	0.045	0.060	1.15	1.52
N	0.190	0.210	4.83	5.33
Q	0.100	0.120	2.54	3.04
R	0.080	0.110	2.04	2.79
S	0.045	0.055	1.15	1.39
T	0.235	0.255	5.97	6.47
U	0.000	0.050	0.00	1.27
٧	0.045		1.15	
Z		0.080		2.04





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