




FR151G THRU FR157G

1.5 AMPS. GLASS PASSIVATED FAST RECOVERY RECTIFIERS



FEATURES

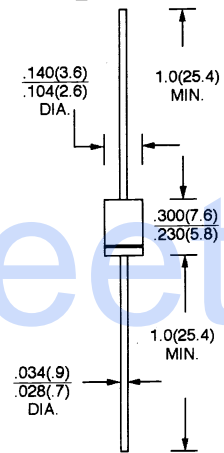
- * Low forward voltage drop
- * High current capability
- * High reliability
- * High surge current capability

MECHANICAL DATA

- * Case: Molded plastic
- * Epoxy: UL 94V-0 rate flame retardant
- * Lead: Axial leads, solderable per MIL-STD-202, method 208 guaranteed
- * Polarity: Color band denotes cathode end
- * Mounting Position: Any
- * Weight: 0.40 grams

VOLTAGE RANGE
50 to 1000 Volts
CURRENT
1.5 Amperes

DO-15



Dimensions in inches and (millimeters)

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Rating at 25°C ambient temperature unless otherwise specified.
Single phase, half wave, 60 Hz, resistive or inductive load.
For capacitive load, derate current by 20%

TYPE NUMBER	SYMBOLS	FR 151G	FR 152G	FR 153G	FR 154G	FR 155G	FR 156G	FR 157G	UNITS
Maximum Recurrent Peak Reverse Voltage	V _{RRM}	50	100	200	400	600	800	1000	V
Maximum RMS Voltage	V _{RMS}	35	70	140	280	420	560	700	V
Maximum D. C Blocking Voltage	V _{DC}	50	100	200	400	600	800	1000	V
Maximum Average Forward Rectified Current .375"(9.5mm) lead length @ T _A = 55°C	I _{F(AV)}	1.5							A
Peak Forward Surge Current, 8.3 ms single half sine-wave superimposed on rated load (JEDEC method)	I _{FSM}	50							A
Maximum Instantaneous Forward Voltage at 1.5A	V _F	1.3							V
Maximum D. C Reverse Current at Rated D. C Blocking Voltage	I _R					5.0			μA
						100			μA
Maximum Reverse Recovery Time (Note 1)	T _{RR}	150				250	500		nS
Typical Junction Capacitance (Note 2)	C _J	25							pF
Operating and Storage Temperature Range	T _J , T _{STG}	- 65 to + 150							°C

NOTES: 1. Reverse Recovery Test Conditions: I_F = 0.5A, I_R = 1.0A, I_{RR} = 0.25A.
2. Measured at 1 MHz and applied reverse voltage of 4.0V D. C.

RATINGS AND CHARACTERISTIC CURVES (FR151G THRU FR157G)

FIG. 1 – TYPICAL FORWARD CURRENT DERATING CURVE

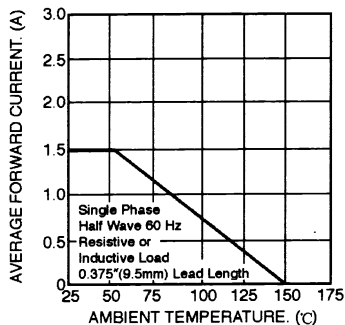


FIG. 2 – MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

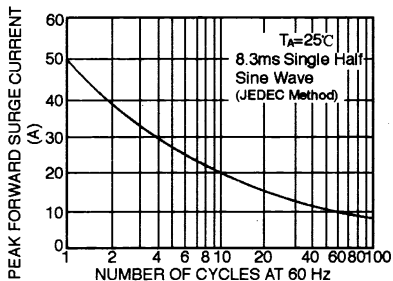


FIG. 3 – TYPICAL FORWARD CHARACTERISTICS

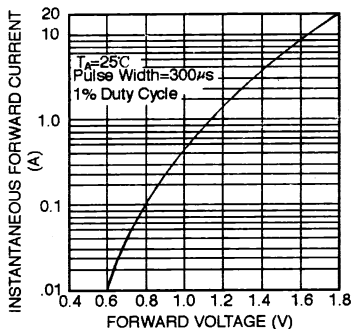


FIG. 4 – TYPICAL JUNCTION CAPACITANCE

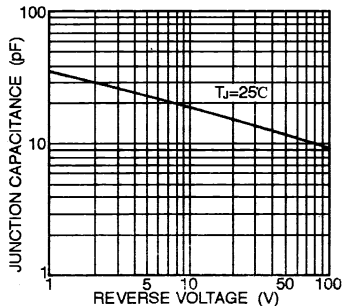


FIG. 5 – TEST CIRCUIT DIAGRAM AND REVERSE RECOVERY TIME CHARACTERISTICS

