

Resistor/Capacitor Networks, Molded SIP



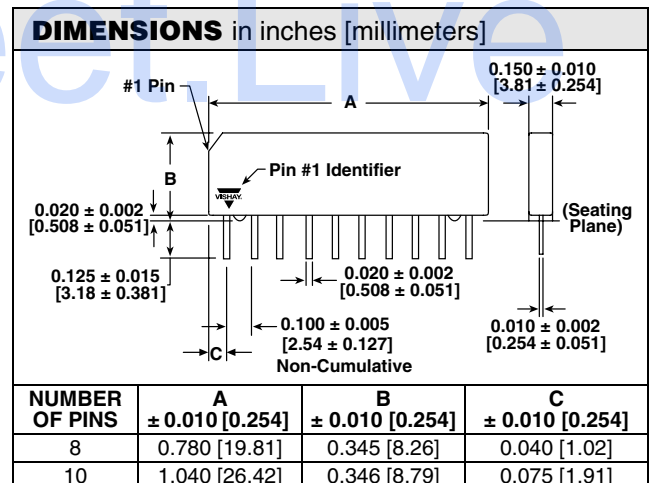
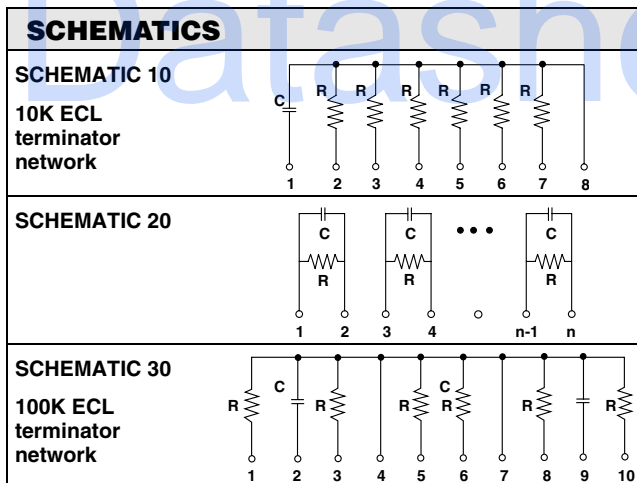
FEATURES

- Custom schematics available
- NP0 or X7R capacitors for line terminator
- Wide operating temperature range (- 55 °C to 125 °C)
- Molded epoxy case
- Solder coated copper terminals
- Solderability per MIL-STD-202 method 208E
- Marking resistance to solvents per MIL-STD-202 method 215

STANDARD ELECTRICAL SPECIFICATIONS									
MODEL	SCHEMATIC	RESISTOR CHARACTERISTICS				CAPACITOR CHARACTERISTICS			
		POWER RATING P _{70 °C} W	RESISTANCE (Ω)	RESISTANCE TOLERANCE ± %	TEMP. COEFF. ± ppm/°C	TYPE (2)	CAPACITANCE RANGE	CAPACITANCE TOLERANCE (3) ± %	CAPACITANCE VOLTAGE VDC
MRCN	10	0.20	50 - 1K	2 %, 5 % (1 %) ⁽¹⁾	150 ppm	NP0	33 pF - 3900 pF	10 %, 20 %	50
						X7R	470 pF - 0.1 μF	10 %, 20 %	
	20	0.20	50 - 1K	2 %, 5 % (1 %) ⁽¹⁾	150 ppm	NP0	33 pF - 3900 pF	10 %, 20 %	50
						X7R	470 pF - 0.1 μF	10 %, 20 %	
	30	0.20	50 - 1K	2 %, 5 % (1 %) ⁽¹⁾	150 ppm	NP0	33 pF - 3900 pF	10 %, 20 %	50
						X7R	470 pF - 0.1 μF	10 %, 20 %	

Notes

- (1) ± 1 % tolerance available on request
 (2) NP0 Capacitors may be substituted for X7R capacitors
 (3) Tighter tolerances available on request



Note

- Custom schematics available

GLOBAL PART NUMBER INFORMATION

New Global Part Numbering: MRCN081N101J560KT B

GLOBAL MODEL	PIN COUNT	SCHEMATIC	CHARACTERISTIC	RESISTANCE VALUE	RESISTANCE TOLERANCE	CAPACITANCE VALUE	CAPACITANCE TOLERANCE	TERMINAL FINISH	PACKAGING
MRCN	08 = 8 pin 10 = 10 pin	1 = 10 2 = 20 3 = 30	N = NP0 X = X7R	2 digit significant figure, followed by a multiplier 101 = 100 Ω 220 = 22 Ω 102 = 1 kΩ	F = 1 % G = 2 % J = 5 %	(in picofarads) 2 digit significant figure, followed by a multiplier 101 = 100 pF 392 = 3000 pF 104 = 0.1 μF	K = 10 % M = 20 %	T = Sn90/Pb10	B = Bulk T = Tray

Historical Part Numbering: MRCN0801101J560KS10 (will continue to be accepted)

HISTORICAL MODEL	PIN COUNT	SCHEMATIC	RESISTANCE VALUE	RESISTANCE TOLERANCE	CAPACITANCE VALUE	CAPACITANCE TOLERANCE	TERMINAL FINISH
MRCN	08	01	101	J	560	K	S10



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