

MC10ELT and MC100ELT: ECLinPS Lite Translators 10 K and 100 K Emitter-Coupled Logic in Picoseconds Integrated Circuits

Mfr.'s Type		Description	No. of Leads	Mfr.'s Type		Description	No. of Leads
MC10ELT	MC100ELT			MC10ELT	MC100ELT		
SOIC	SOIC			SOIC	SOIC		
—	MC100ELT21D MC100ELT22D	Differential PECL To TTL Translator Dual TTL To Differential PECL Translator	8 8	MC10ELT25D	MC100ELT23D MC100ELT25D	Dual Differential PECL-to-TTL Translator Dual Differential ECL-to-TTL Translator	8 8

MC10H: MECL 10H Monolithic Emitter-Coupled Logic Integrated Circuits

Mfr.'s Type		Description	No. of Leads	Mfr.'s Type		Description	No. of Leads
PDIP	PLCC			PDIP	PLCC		
MC10H101P MC10H115P MC10H116P MC10H124P	— MC10H115FN — MC10H124FN	Quad OR/NOR Gate Quad Line Receiver Triple Line Receiver Quad TTL-to-MECL Translator with TTL Strobe Input	16/20 16/20 16/20 16/20	MC10H125P MC10H131P —	— — MC10H351FN	Quad MECL-to-TTL Translator Dual Type D Master-Slave Flip-Flop Quad TTL/NMOS To PECL Translator	16/20 16/20 20/20 —

MC10: MECL 10 K Monolithic Emitter-Coupled Logic Integrated Circuits

Mfr.'s Type	Description	No. of Leads	Mfr.'s Type	Description	No. of Leads
PDIP			PDIP		
MC10102P MC10103P MC10104P MC10107P MC10113P	MECL Quad 2-Input NOR Gate MECL Quad 2-Input OR Gate MECL Quad 2-Input AND Gate MECL Triple 2-Input Exclusive OR/Exclusive NOR MECL Quad Exclusive OR Gate	16/20 16 16 16 16	MC10116P MC10124P MC10125P MC10131P MC10216P	MECL Triple Line Receiver MECL Quad TTL To MECL Translator MECL Quad MECL To TTL Translator MECL Dual Type D Master-Slave Flip-Flop MECL High Speed Triple Line Receiver	16/20 16/20 16/20 16/20 16/20

Line Receivers — EIA Standard

Mfr.'s Type		Single Ended or Differential	Type of Output	t _{prop} Delay Time Max. (ns)	Party Line Operation	Strobe or Enable	Power Supplies (V)	Receivers Per Package	Companion Drivers	Comments	No. of Leads
PDIP	SOIC										
MC1489AP MC1489P	— MC1489D	Single Ended Single Ended	Resistor Pull-Up Resistor Pull-Up	85 85	— —	— —	+5.0 +5.0	4 4	MC1488 MC1488	EIA-232-D EIA-232-D	14 14

Line Drivers

EIA Standard

Mfr.'s Type		Output Current Capability (mA)	Single Ended or Differential	t _{prop} Delay Time Max. (ns)	Party Line Operation	Strobe or Enable	Power Supplies (V)	Receivers Per Package	Companion Drivers	Comments	No. of Leads
PDIP	SOIC										
MC1488P —	MC1488D MC26LS30D	10 60	Single Ended Single Ended, Differential	350 300	— —	— 422 Yes, 423 No	±9.0 to ±12 ±5.0	4 2 (422), 4 (423)	MC1489 AM26LS32	EIA-232-D EIA-422, EIA-423 Switchable	14 16

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Peripheral Drivers

Mfr.'s Type	Output Current Capability (mA)	Input Capability	Propagation Delay Time Max. (us)	Output Clamp Diode	Off State Voltage Max. (V)	Drivers Per Package	Logic Function	Package Type
MC1413P	500	TTL, +5 V CMOS	1	Yes	50	7	Invert	16 Lead PDIP

CMOS Display Drivers

Mfr.'s Type	Function	Display Type	Input Format	Drive Capability Per Package	On-Chip Latch	Display Control	Segment Drive Current	Package Type
MC14543BCP MC14511BCP MC14513BCP	BCD-to-7 Segment Latch/Decoder/Driver BCD-to-7 Segment Latch/Decoder/Driver BCD-to-7 Segment Latch/Decoder/Driver	LCD, Direct Drive LED, Incandescent, Fluorescent* LED, Incandescent, Fluorescent*	Parallel BCD Parallel BCD Parallel BCD	7 Segments 7 Segments 7 Segments	Yes Yes Yes	Blank Blank, Lamp Test Blank, Ripple Blank, Lamp Test	Approx. 1.0 mA 25 mA 25 mA	16 Lead PDIP 16 Lead PDIP 18 Lead PDIP

*Absolute maximum working voltage=18 V.

Balanced Modulator/Demodulator

Mfr.'s Type		V _{cc} (V)	I _{cc} (mA)	Carrier Suppression	Common Mode Rejection	Function	No. of Leads
PDIP	SOIC						
MC1496P	MC1496D	5 to 30	10	65 dB typ. @ 0.5 MHz 50 dB typ. @ 10.0 MHz	85 dB typ.	Carrier Balance >50 dB, General Purpose Balanced Modulator/Demodulator For AM, SSB and FM Detection.	14

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