MIL-S-19500/126C 24 March 1971 SUPERSEDING MIL-S-19500/126B 20 March 1964

### MILITARY SPECIFICATION

SEMICONDUCTOR DEVICE, TRANSISTOR, GERMANIUM, HIGH-FREQUENCY NPN TYPES 2N1302, 2N1304, 2N1306, 2N1308 AND PNP TYPES 2N1303, 2N1305, 2N1307, 2N1309

This specification is mandatory for use by all Departments and Agencies of the Department of Defense.

### 1. SCOPE

- 1.1 Scope. This specification covers the detail requirements for NPN and PNP, complementary germanium, high-frequency transistors.
  - 1.2 Physical dimensions. See figure 1 (TO-5).
  - 1.3 Maximum ratings.

P <sub>T</sub> 1/	v	СВ	v	$v_{EB}$		С	T <sub>stg</sub>
mw	NPN Vdc	PNP Vdc	NPN Vdc	PNP Vdc	NPN mAdc	PNP mAdc	<u>°C</u>
150	25	-30	25	-25	300	-300	-65 to +100

1/ Derate linearly 2.5 mW/° C for TA between 25° C and 85° C.

## 1.4 Primary electrical characteristics.

		h	re	····	f <sub>hfb</sub>				
	-	= 1 Vdc, I	•	$V_{CB} = 5 \text{ Vdc}, I_{E} = -1 \text{ mAdc} \frac{1}{2}$					
	$v_{CE}$	= -1 Vdc,	I <sub>C</sub> = -10 m	Adc 2/	$V_{CB} = -5 \text{ Vdc}, I_{E} = 1 \text{ mAdc} \frac{2}{}$			dc <u>2</u> /	
	2N1302 2N1303	2N1304 2N1305	2N1306 2N1307	2N1308 2N1309	2N1302 2N1303	2N1304 2N1305	2N1306 2N1307	2N1308 2N1309	
			es en en		MHz	MHz	MHz	MHz	
Minimum	20	40	60	80	3	5	10	15 .	
Maximum		200	300	400					

- 1/ Test conditions for types 2N1302, 2N1304, 2N1306 and 2N1308.
- 2/ Test conditions for types 2N1303, 2N1305, 2N1307 and 2N1309.

## 2. APPLICABLE DOCUMENTS

2.1 The following documents, of the issue in effect on date of invitation for bids or request for proposal, form a part of the specification to the extent specified herein.

#### SPECIFICATION

MIL-S-19500 - Semiconductor Devices, General Specification for.

#### **STANDARDS**

#### MILITARY

MIL-STD-202 - Test Methods for Electronic and Electrical Component Parts. MIL-STD-750 - Test Methods for Semiconductor Devices.

(Copies of specifications, standards, drawings, and publications required by suppliers in connection with specific procurement functions should be obtained from the procuring activity or as directed by the contracting officer.)

#### 3. REQUIREMENTS

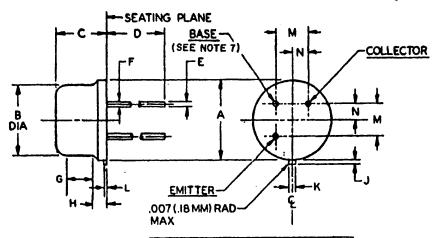
- 3.1 General. Requirements shall be in accordance with MIL-S-19500, and as specified herein.
- 3.2 Abbreviations, symbols, and definitions. The abbreviations, symbols, and definitions used herein are defined in MIL-S-19500, and as follows:

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Qsb - - - · - - - - Stored base charge pC - - - - - - - Pico-coulombs
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- 3.3 Design, construction, and physical dimensions. Transistors shall be of the design, construction, and physical dimensions shown on figure 1.
- 3.3.1 <u>Lead material and finish</u>. Lead material shall be Kovar or alloy 52. Lead finish shall be gold-plated. (Leads may be tin-coated if specified in the contract or order, and this requirement shall not be construed as adversely affecting the qualified-product status of the device, or applicable JAN marking, see 6.2.)
- 3.4 Performance characteristics. Performance characteristics shall be as specified in tables I, II, and  $\overline{\text{III}}$ .
- 3.5 Marking. The following marking specified in MIL-S-19500 may be omitted from the body of the transistor at the option of the manufacturer:
  - (a) Country of origin.
  - (b) Manufacturer's identification.

## 4. QUALITY ASSURANCE PROVISIONS

- 4.1 Sampling and inspection. Sampling and inspection shall be in accordance with MIL-S-19500, and as specified herein.
- 4.2 Qualification inspection. Qualification inspection shall consist of the examinations and tests specified in tables I, II, and III.



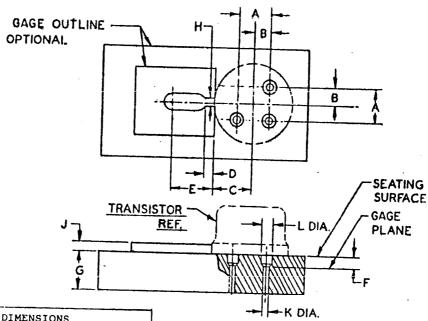
		DIMENSI	ONS		N
LTR	INC	HES	MILLIM	ETERS	ZOMES
	MIN	MAX	MIN	XAM	
A	. 335 <sup>.</sup>	.370	8.51	9.40	
В	.305	.335	7.75	8.51	
С	.240	.260	6.10	6.60	
D	1.500	1.750	38.10	44.45	9
E	.016	.021	.41	.53	2,9
F	.016	.019	.41	.48	3,9
G	.100		2.54		4
н					5
J	.029	.045	.74	1.14	8
K	.028	.034	.71	.86	
L	.009	.041	. 23	1.04	
М	.1	414 Nom	3.59	6	
N	.0	707 Nom	1.80	Nom	6

## NOTES:

- 1. Metric equivalents (to the nearest .01 mm) are given for general information only and are based upon 1 inch = 25.4 mm.
  2. Measured in the zone beyond .250 (6.35 mm) from the seating plane.
  3. Measured in the zone .050 (1.27 mm) and .250 (6.35 mm) from the seating plane.

- 4. Variations on dimension B in this zone shall not exceed .010 (.25 mm).
- 5. Outline in this zone is not controlled.
- 6. When measured in a gaging plane .054+.001, -.000 (1.37+.03, -.00 mm) below the seating plane of the transistor, maximum diameter leads shall be within .007 (.18 mm) of their true location relative to a maximum width tab. Smaller diameter leads shall fall within the outline of the maximum diameter lead tolerance. Figure 2 shows the preferred measured method.
- The base shall be electrically connected to the case. Measured from the maximum diameter of the actual device.
- 9. All 3 leads.

Physical dimensions of transistor types 2N1302 through 2N1309(TO-5). FIGURE 1.

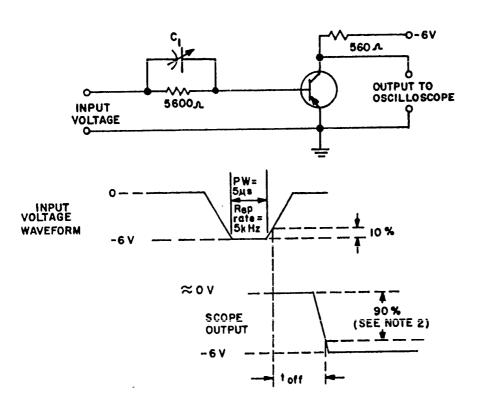


_					
<u> </u>		DIMENS	IONS		
LTR	IN	CHES	MILLIM	ETERS	
	MIN	MAX	MIN	MAX	
A	.1409	.1419	3.58	3.60	
В	.0702	.0712	1.78	1.81	
С	.182	.199	4.62	5.05	
D	,009	.011	.23	.28	
E	. 1	25 Nom	3.	18 Nom	
F	.054	.055	1.37	1.40	
G	.372	.378	9.45	9.60	
Н	.0350	.0355	.89	.90	
J	.1	50 Nom	Nom 3.81 No.		
ĸ	.0325	.0335	.83	.85	
L	.0595	.0605	1.51	1.54	

# NOTES:

- 1. The following gaging procedure shall be used: The use of a pin straightener prior to insertion in the gage is permissible. The device being measured shall be inserted until its seating plane is .125±.010 (3.18±.25 mm) from the seating surface of the gage. A spacer may be used to obtain the .125 (3.18 mm) distance from the gage seat prior to force application. A force of 8 oz ±.05 oz shall then be applied parallel and symmetrical to the device's cylindrical axis. When examined visually after the force application (the force need not be removed) the seating plane of the device shall be seated against the gage.
- The location of the tab locator, within the limits of dim C, will be determined by the tab and flange dimension of the device being checked.
- Metric equivalents (to the nearest .01 mm) are given for general information only and are based upon 1 inch = 25.4 mm.

FIGURE 2. Gage for lead and tab location for transistor types 2N1302 through 2N1309.



NOTES:

The capacitance of  $C_1$  shall be increased until the <sup>t</sup> off time of the output waveform is decreased to .2 usec.  $Q_{Sb}$  in pC is then calculated by  $Q_{Sb} = C_1 \times E_{in}$  where  $C_1$  is in pF and  $E_{in}$  is 6 volts. Any unit that turns on above the 90-percent point, once it starts to turn off, fails the test.

FIGURE 3. Circuit for determining value of stored base charge. (Polarities shown apply to PNP)

	TAF	BLE I. Group A inspection.	•					
Examination or test		MIL-STD-750				Limits	$\neg$	
	Method	Details	LTPI	2 Symbo	l Mir	Mo	x Un	ıit.
Subgroup 1			10	<del> </del>	+	+-		
Visual and mechanical examination	2071					-   -		-
Subgroup 2	-		5					
Breakdown voltage, collector to base	3001							
All NPN devices		Bias cond. D; I <sub>C</sub> = 100 μAdc	·	BVCBC	25		- Vdc	;
All PNP devices		Bias cond. D; I <sub>C</sub> = -100 μAdc		BVCBO	-30	)	- Vdc	2
Breakdown voltage, emitter to base	3026							
All NPN devices		Bias cond. D; I <sub>E</sub> = 100 μAdc		BVEBO	25		- Vdc	
All PNP devices		Bias cond. D; I <sub>E</sub> = -100 µAdc		ву <sub>ЕВО</sub>	-25		- Vdc	
Collector to bas, cutoff current	3036							
All NPN devices		Bias cond. D; V <sub>CB</sub> = 25 Vdc		I <sub>CBO</sub>		•	μAd	c
All PNP devices		Bias cond. D; V <sub>CB</sub> = -25 Vdc		I <sub>СВО</sub>		- 6	μAdo	2
Emitter to base cutoff current	3061							
All NPN devices		Bias cond. D; V <sub>EB</sub> = 25 Vdc		I <sub>EBO</sub>		6	μAdc	
All PNP devices		Bias cond. D; V <sub>EB</sub> = -25 Vdc		I <sub>EBO</sub>		-6	μAdc	
Floating potential	3020	R <sub>BE</sub> = 1 megohm ±5% including voltmeter input resistance						
NPN devices - 2N1302		$V_{CB} = 25 \text{ Vdc}$		v <sub>EBF</sub>		1	Vdc	
2N1304		$V_{CB} = 20 \text{ Vdc}$		LDF		1	Vdc	l
2N1306		V <sub>CB</sub> = 15 Vdc			,-	1	Vdc	
2N1308		V <sub>CB</sub> = 15 Vdc				1	Vdc	
PNP devices - 2N1303	1 1	V <sub>CB</sub> = -25 Vdc	,	EBF		-1	Vdc	
2N1305	'	$V_{\rm CB} = -20  \text{Vdc}$		•		-1	Vdc	
2N1307	1 1	V <sub>CB</sub> = -15 Vdc				-1	Vdc	
2N1309		V <sub>CB</sub> = -15 Vdc				-1	Vdc	
	•	•	1 j	i	ł	1	- 1	

		TABLE	I. Group A inspection	-Continued				·
Englishter	***		MIL-STD-750			Li	mits	
Examination or	iesī	Method	Details	LTPD	Symbol	Min	Max	Unit
Subgroup 3				5				
Forward-current ratio	transfer	3076						
NPN devices -	2N1302		V <sub>CE</sub> = 1 Vdc; I <sub>C</sub> = 10 mAdc		$h_{\mathbf{FE}}$	20		
:	2N1304					40	200	
	2N1306					60	300	
:	2N1308					80	400	
PNP devices -	2N1303		$V_{CE} = -1 \text{ Vdc};$		$\mathtt{h_{FE}}$	20		
	2N1305		$I_C = -10 \text{ mAdc}$			40	200	
:	2N1307					60	300	
:	2N1309					80	400	
NPN devices -	2N1302		V <sub>CE</sub> = 0.35 Vdc; I <sub>C</sub> = 200 mAdc		h <sub>FE</sub>	10		
	2N1304					15		
	2N1306					20		
1	2N1308					20		
PNP devices -	2N1303		$V_{CE} = -0.35 \text{ Vdc};$ $I_{C} = -200 \text{ mAdc}$		h <sub>FE</sub>	10		
	2N1305					15		
]	2N1307					20		
	2N1309					20		
Collector to emitte (saturated)	er voltage	3071						
NPN devices -	2N1302		I <sub>B</sub> = 0.5 mAdc; I <sub>C</sub> = 10 mAdc		VCE (sat)		0. 20	Vdc
	2N1304		I <sub>B</sub> = 0.25 mAdc; I <sub>C</sub> = 10 mAdc				0.20	Vdc
	2N1306		I <sub>B</sub> = 0.17 mAdc; I <sub>C</sub> = 10 mAdc				0. 20	Vdc
	2N1308		I <sub>B</sub> = 0.13 mAde; I <sub>C</sub> = 10 mAde				0. 20	Vdc
	•							
1				1				1

		LE I. Group A inspection.	-Continue	<u>d</u>				
Examination or test	t.	MIL-STD-750		1		Limits		
	Method	· Details	LTPI	Symbo	Mir	, Mo	x Uni	t
Subgroup 3 - Continued					+			
PNP devices - 2N1303		I <sub>B</sub> = -0.5 mAde; I <sub>C</sub> = -10 mAde		V <sub>CE</sub> (sat)		-0.2	0 Vdc	:
2N1305		I <sub>B</sub> = -0.25 mAdc; I <sub>C</sub> = -10 mAdc				-0.2	0 Vdc	
2N1307		$I_B = -0.17 \text{ mAdc};$ $I_C = -10 \text{ mAdc}$				-0.2	0 Vdc	
2N1309		I <sub>B</sub> = -0.13 mAdc; I <sub>C</sub> = -10 mAdc				0.20	Vdc	
Base emitter voltage (saturated)	3066	Test cond. A						
NPN devices - 2N1302		I <sub>B</sub> = 0.5 mAdc; I <sub>C</sub> = 10 mAdc		V <sub>BE</sub> (sat)	0.1	5 0.4	0 Vdc	
2N1304					0.1	5 0 3	5 Vdc	
2N1306			ĺ		0.18		ļ	
2N1308					0.18		5 Vdc	
PNP devices - 2N1303		I <sub>B</sub> = -0.5 mAde; I <sub>C</sub> = -10 mAde		V <sub>BE</sub> (sat)	-0.15	1	Vdc Vdc	
2N1305			1	(520)			.	İ
2N1307					ł	-0.3	1	
2N1309					-0.15	1	Vdc	
Subgroup 4			5		-0.15	-0.38	Vdc	1
Open circuit output capacitance	3236			·				
All NPN devices		$V_{CB} = 5 \text{ Vdc}; I_{E} = 0$ 100 kHz $\leq f \leq 1 \text{ MHz}$		C <sub>obo</sub>		20	pF	
All PNP devices		$V_{CB}$ = -5 Vdc; $I_{E}$ = 0 100 kHz $\leq$ f $\leq$ 1 MHz		C <sup>ópo</sup>		20	pF.	
Small-signal short-circuit forward-current transfer- ratio cutoff frequency	3301	i						
NPN devices - 2N1302		V <sub>CB</sub> = 5 Vdc; I <sub>E</sub> = -1 mAdc						
2N1304		, - <u>-</u>		fhfb	3		MHz	
2N1306					5		MHz	
2N1308					10		MHz	
1	1				15		MHz	

MADERY	C1	A	
IADLE I.	Group	A inspection.	-Continued

E		MIL-STD-750			L	imits	
Examination or test	Method	Details		Symbol	Min	Max	Unit
Subgroup 4 - Continued						1	
PNP devices - 2N1303		$V_{CB} = -5 \text{ Vdc}; I_E = 1 \text{ mAdc}$		fhfb	3		MHz
2N1305		·			5		MHz
2N1307					10		MHz
2N1309			<u> </u>		15		MHz
Stored base charge		(See figure 3)					
NPN devices - 2N1302				Q <sub>sb</sub>		1400	рC
2N1304						1200	pC
2N1306						1000	pC
2N1308						1000	pC
PNP devices - 2N1303				Q <sub>sb</sub>		1600	pC
2N1305				-60		1400	pC
2N1307						1200	pC
2N1309						1200	pC
Subgroup 5			10			1200	
High-temperature operation:		$T_A = +55^{\circ} C$					
Collector to emitter cutoff current	3041	Bias cond. A					
NPN devices		V <sub>CE</sub> = 15 Vdc V <sub>BE</sub> = -0.2 Vdc		1 <sub>CEX</sub>		50	μAdc
PNP devices		V <sub>CE</sub> = -15 Vdc V <sub>BE</sub> = 0.2 Vdc		ICEX	  -==	-50	μ <b>A</b> dc
Low-temperature operation:		T <sub>A</sub> = -65° C					
Forward-current transfer ratio	3076						
NPN devices 2N1302		V <sub>CE</sub> = 1 Vdc I <sub>C</sub> = 10 mAdc		h <sub>FE</sub>	10		
2N1304					20	200	
2N1306					30	300	
2N1308		**	ļ		40		
PNP devices 2N1303		$V_{CE} = -1 \text{ Vdc}$ $I_{C} = -10 \text{ mAdc}$		h <sub>FE</sub>	10		
2N1305					20	200	
2N1307			ŀ		30	300	
2N1309					40		

TABLE II. Group B inspection.

<u></u>	TABLE				1		T
Examination or test		MIL-STD-750	-			imits	1
	Method	Details	LTPD	Symbol	Min	Mox	Unit
Subgroup 1			20				
Physical dimensions	2066	(See figure 1)					
Subgroup 2			15				
Solderability	2026						
Thermal shock (temperature cycling)	1051	Test cond. C, except in step 3, T <sub>A</sub> = +85 ±5°C					
Thermal shock (glass strain)	1056	Test cond. A					
Moisture resistance	1021				′		
End points:							
Collector to base cutoff current	3036	Bias cond. D					
All NPN devices		$V_{CB} = 25 \text{ Vdc}; I_E = 0$		СВО		6	μAdc
All PNP devices		V <sub>CB</sub> = -25 Vdc; I <sub>E</sub> = 0		СВО		-6	μAdc
Forward-current transfer ratio	3076						
NPN devices - 2N1302		V <sub>CE</sub> = 1 Vdc I <sub>C</sub> = 10 mAdc		h <sub>FE</sub>	20		
2N1304 2N1306 2N1308					40 60 80	200 300 400	
PNP devices - 2N1303		$V_{CE} = -1 \text{ Vdc}$ $I_{C} = -10 \text{ mAdc}$		h <sub>FE</sub>	20		
2N1305 2N1307 2N1309	.,				40 60 80	· 200 300 400	
Subgroup 3			15	•			
Shock	2016	Nonoperating; 1,500 G; 0.5 ms; 5 blows in each orientation: $X_1$ , $Y_1$ , $Y_2$ and $Z_1$					
Vibration, variable frequency	2056						
Constant acceleration	2006	10,000 G in each orientation: $X_1$ , $Y_1$ , $Y_2$ , and $Z_1$					
End points: (Same as subgroup 2)							
Subgroup 4			20			į	
Terminal strength (lead fatigue)	2036	Test cond. E					

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TA	BLE II.	Group B inspection, -Continu	ied	·	•••••	~ 100	186C
Examination or test		MIL-STD-750			L	imits	
Examination of test	Method	Details	LTPD	Symbol	Min `	Mex	Unit
Subgroup 4 -Continued						<del>                                     </del>	1
End points:							
Hermetic seal	1071	Test cond. G or H for fine leaks; test cond. A, C, D, or F for gross leaks				1×10 <sup>-7</sup>	atm cc/s
Subgroup 5			20				
Salt atmosphere (corrosion)	1041						
Subgroup 6			7				
High-temperature life (nonoperating)	1032	T <sub>stg</sub> = +100° C time = 340 hours (see 4.3.4)					
End points:							
Collector to base cutoff current	3036	Bias cond. D					
All NPN devices		V <sub>CB</sub> = 25 Vdc; I <sub>E</sub> = 0		I <sub>CBO</sub>		12	μAdc
All PNP devices		$V_{CB} = -25 \text{ Vdc}; I_{E} = 0$		I <sub>CBO</sub>		-12	μAdc
Forward-current transfer ratio	3076						
NPN devices - 2N1302		V <sub>CE</sub> = 1 Vdc		h <sub>FE</sub>	16		
2N1304 2N1306 2N1308		I <sub>C</sub> = 10 mAde			32 48 64		
PNP devices 2N1303		V <sub>CE</sub> = -1 Vdc	1	h <sub>FE</sub>	16		
2N1305		I <sub>C</sub> = -10 mAdc	!		32		
2N1307 2N1309					48 64		
Subgroup 7			7				
Steady-state operation life	1027	P <sub>T</sub> = 150 mW time = 340 hours (see 4. 3. 4)					
All NPN devices		V <sub>CB</sub> = 10 Vdc					
All PNP devices		V <sub>CB</sub> = -10 Vdc					
End points: (Same as subgroup 6)							

TABLE III. Group C inspection.

_		MIL-STD-750			Li	nits	
Examination or test	Method	Details	LTPD	Symbol	Min	Max	Unit
Subgroup 1			10				
Resistance to solvents		MIL-STD-202, Method 215 (see 4.4.1)					
Subgroup 2			λ = 10				
High-temperature life (nonoperating)	1031	$T_{stg} = +100^{\circ} C \text{ (see 4.3.4)}$					
End points: (Same as subgroup 6 of Group B)							
Subgroup 3			λ = 10	:			
Steady-state operation life	1026	P <sub>T</sub> = 150 mW (see 4.3.4)					
All NPN devices		V <sub>CB</sub> = 10 Vdc					
All PNP devices		V <sub>CB</sub> = -10 Vdc					
End points: (Same as subgroup 6 of Group B)							

- 4.3 Quality conformance inspection. Quality conformance inspection shall consist of group A, B, and C inspections. A lot shall consist of transistors of only one family (NPN or PNP). For subgroups 1 through 5 of group B tests and subgroup 1 of group C, where a lot consists of more than one type, only one type need be tested as a representative of the lot for all types in the lot. For subgroups 6 and 7 of group B tests and subgroups 2 and 3 of group C tests, the samples for each subgroup shall consist of all types within the lot to be tested and the number of devices of each type shall be proportional to the distribution of the types within the lot.
- 4.3.1 Group A inspection. Group A inspection shall consist of the examinations and tests specified in table I.
- 4.3.2 Group B inspection. Group B inspection shall consist of the examinations and tests specified in table II.
- 4.3.3 Group C inspection. Group C inspection shall consist of the tests specified in table III. This inspection shall be conducted on the initial lot and thereafter every six months during production.
- 4.3.4 Group B and group C life-test samples. Samples that have been subjected to group B, 340-hours life-test, may be continued on test to 1,000 hours in order to satisfy group C life-test requirements. These samples shall be predesignated, and shall remain subjected to the group C 1,000-hour acceptance evaluation after they have passed the group B, 340-hour acceptance criteria. The cumulative total of failures found during 340-hour test and during the subsequent interval up to 1,000 hours shall be computed for 1,000-hour acceptance criteria, see 4.3.3.
- 4.4 Methods of examination and test. Methods of examination and test shall be as specified in tables I, II, and as follows:
- 4.4.1 Resistance to solvents. Transistors shall be subjected to tests in accordance with method 215 of MIL-STD-202. The following details shall apply:
  - (a) All areas of the transistor body where marking has been applied shall be brushed.
  - (b) After subjection to the tests there shall be no evidence of mechanical damage to the device and and markings shall have remained legible.

- 5. PREPARATION FOR DELIVERY
- 5.1 See MIL-S-19500, section 5,
- 6. NOTES
- 6.1 Notes. The notes specified in MIL-S-19500 are applicable to this specification.
- 6.2 Ordering data.
  - (a) Lead finish if other than gold-plated (see 3.3.1).

Custodians:

Army - EL Navy - EC Air Force - 17

Review activities:

Army - MU, MI

Air Force - 11, 70, 80 DSA - ES

User activities:

Army - SM Navy - AS, CG, MC, OS, SH Air Force - 13, 15, 19

Preparing activity: Navy - EC

Agent: DSA - ES

(Project 5961-0214)

SPECIFICATION ANALYSIS SHEET		Form Approved Budget Bureau No. 22-R255
INSTRUCTIONS: This sheet is to be filled out by personnel, either Government or contractor, involved in the use of the specification in procurement of products for ultimate use by the Department of Defense. This sheet is provided for obtaining information on the use of this specification which will insure that suitable products can be procured with a minimum amount of delay and at the least cost. Comments and the return of this form will be appreciated. Fold on lines on reverse side, staple in corner, and send to preparing activity. Comments and suggestions submitted on this form do not constitute or imply authorization to waive any portion of the referenced document(s) or serve to amend contractual requirements.		
\$PECIFICATION		
ORGANIZATION .		
CITY AND STATE	CONTRACT NUMBER	
MATERIAL PROCURED UNDER A  DIRÉCT GOVERNMENT CONTRACT SUBCONTRACT		
1. HAS ANY PART OF THE SPECIFICATION CREATED PROBLEMS OR REQUIRED INTERPRETATION IN PROCURE- MENT USE! A. GIVE PARAGRAPH NUMBER AND WORDING.		
B. RECOMMENDATIONS FOR CORRECTING THE DEFICIENCIES		
D. RESOMMEROATIONS FOR CONRECTING THE SCRICIENCIES		
2. COMMENTS ON ANY SPECIFICATION REQUIREMENT CONSIDERED TOO RIGID		
IS THE SPECIFICATION RESTRICTIVE?		
YES NO (Il "yea", in what way?)		
. REMARKS (Attach any pertinent data which may be of use in improving this specification. If there are additional papers, attach to form and place both in an envelope additional preparing activity)		
IUBMITTED BY (Printed or typed name and activity - Optiona	)	DATE