

## TPS3803-01-EP TPS3803G15-EP TPS3805H33-EP

SGLS227C - DECEMBER 2003 - REVISED JUNE 2007

# VOLTAGE DETECTOR

#### **FEATURES**

- Controlled Baseline
  - One Assembly/Test Site, One Fabrication Site
- Extended Temperature Performance of Up to -55°C to +125°C
- Enhanced Diminishing Manufacturing Sources (DMS) Support
- Enhanced Product-Change Notification
- Qualification Pedigree<sup>(1)</sup>
- Single Voltage Detector (TPS3803): Adjustable/1.5 V
- Dual Voltage Detector (TPS3805):
   Adjustable/3.3 V
- High ±1.5% Threshold Voltage Accuracy
- Supply Current: 3 μA Typical at V<sub>DD</sub> = 3.3 V
- Push/Pull Reset Output (TPS3805) Open-Drain Reset Output (TPS3803)
- 5-Pin SC-70 Package
- (1) Component qualification in accordance with JEDEC and industry standards to ensure reliable operation over an extended temperature range. This includes, but is not limited to, Highly Accelerated Stress Test (HAST) or biased 85/85, temperature cycle, autoclave or unbiased HAST, electromigration, bond intermetallic life, and mold compound life. Such qualification testing should not be viewed as justifying use of this component beyond specified performance and environmental limits.

#### **APPLICATIONS**

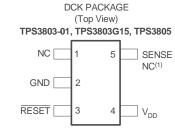
- Applications Using DSPs, Microcontrollers, or Microprocessors
- Wireless Communication Systems
- Portable/Battery-Powered Equipment
- Programmable Controls
- Intelligent Instruments
- Industrial Equipment
- Notebook/Desktop Computers
- Automotive Systems

#### **DESCRIPTION**

The TPS3803 and TPS3805 families of supervisory circuits provide circuit initialization and timing supervision, primarily for DSPs and processor-based systems.

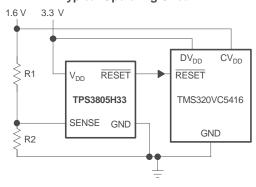
The TPS3803G15 device has a fixed-sense threshold voltage  $V_{IT}$  set by an internal voltage divider, whereas the TPS3803–01 has an adjustable SENSE input that can be configured by two external resistors. In addition to the fixed sense threshold monitored at  $V_{DD}$ , the TPS3805 devices provide a second adjustable SENSE input. RESET is asserted in case any of the two voltages drops below  $V_{IT}$ .

During power on,  $\overline{\text{RESET}}$  is asserted when supply voltage  $V_{DD}$  becomes higher than 0.8 V. Thereafter, the supervisory circuit monitors  $V_{DD}$  (and/or SENSE) and keeps RESET active as long as  $V_{DD}$  or SENSE remains below the threshold voltage  $V_{IT}$ . As soon as  $V_{DD}$  (SENSE) rises above the threshold voltage  $V_{IT}$ , RESET is deasserted again. The product spectrum is designed for 1.5 V, 3.3 V, and adjustable supply voltages. The devices are available in a 5-pin SC-70 package.



(1) NC = No Connection on TPS3803G15

#### **Typical Operating Circuit**

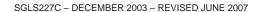




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This integrated circuit can be damaged by ESD. Texas Instruments recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

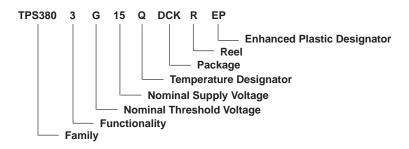
ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

#### **PACKAGE INFORMATION**

_	DEVICE NAME	THRESHOL	MARKING		
TA	DEVICE NAME	$V_{DD}$	SENSE	MARKING	
-40°C to +125°C	TPS3803-01QDCKREP(2)	NA	1.226 V	AWH	
	TPS3803G15QDCKREP(2)	1.4 V	NA	AXT	
	TPS3805H33QDCKREP <sup>(2)</sup>	3.05 V	1.226 V	AWY	
	TPS3803-01MDCKREP(2)	NA	1.226 V	BAY	
−55°C to +125°C	TPS3803G15MDCKREP <sup>(2)</sup>	1.40 V	NA	ARH	
	TPS3805H33MDCKREP <sup>(2)</sup>	3.05 V	1.226 V	ARJ	

<sup>(2)</sup> The DCKR passive indicates tape and reel containing 3000 parts.

#### **ORDERING INFORMATION**



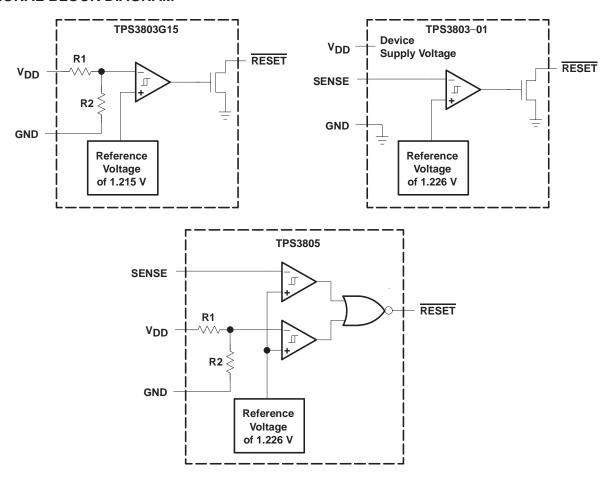
#### **Function/Truth Tables**

TPS3803-	01	TPS3803G15			
SENSE > VIT	RESET	V <sub>DD</sub> > V <sub>IT</sub>	RESET		
0	L	0	L		
1	Н	1	Н		

TPS3805H33									
V <sub>DD</sub> > V <sub>IT</sub>	V <sub>DD</sub> > V <sub>IT</sub> SENSE > V <sub>IT</sub>								
0	0	L							
0	1	L							
1	0	L							
1	1	Н							

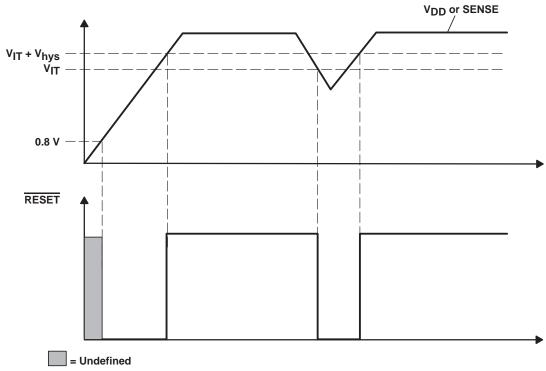


#### **FUNCTIONAL BLOCK DIAGRAM**





#### **TIMING REQUIREMENTS**



**Terminal Functions** 

		-						
TERMINAL NAME NO.		1/0	DESCRIPTION					
INCINIC	140.							
GND	2	- 1	Ground					
RESET	3	0	Active-low reset output (TPS3803—open-drain, TPS3805—push/pull)					
SENSE	5	- 1	Adjustable sense input					
NC	1		No internal connection					
NC (TPS3803G15)	5		No internal connection					
$V_{DD}$	4	I	Input supply voltage, fixed sense input for TPS3803G15 and TPS3805					





#### ABSOLUTE MAXIMUM RATINGS(1)

Over operating free-air temperature, unless otherwise noted.

Supply voltage, V <sub>DD</sub> <sup>(2)</sup>	
All other pins <sup>(2)</sup>	0.3 V to +7 V
Maximum low-output current, I <sub>OL</sub>	+5 mA
Maximum high-output current, IOH	
Input clamp current, $I_{IK}$ ( $V_I < 0$ or $V_I > V_{DD}$ )	±10 mA
Output clamp current, $I_{OK}$ ( $V_O < 0$ or $V_O > V_{DD}$ )	±10 mA
Continuous total power dissipation	
Operating free-air temperature range, T <sub>A</sub>	–55°C to +125°C
Storage temperature range, T <sub>sto</sub> , (3)	–65°C to +150°C

<sup>(1)</sup> Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

#### **DISSIPATION RATING TABLE**

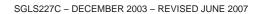
PACKAGE	T <sub>A</sub> < +25°C	DERATING FACTOR	T <sub>A</sub> = +70°C	T <sub>A</sub> = +85°C
	POWER RATING	ABOVE T <sub>A</sub> = +25°C	POWER RATING	POWER RATING
DCK	321 mW	2.6 mW/°C	206 mW	167 mW

#### RECOMMENDED OPERATING CONDITIONS

<u> </u>				
		MIN	MAX	UNIT
Supply voltage, V <sub>DD</sub>		1.3	6	V
Input voltage, V <sub>I</sub>		0	V <sub>DD</sub> + 0.3	V
Operating free cir temperature reage. To	Q suffix devices	-40	+125	°C
Operating free-air temperature range, T <sub>A</sub>	M suffix devices	-55	+125	-0

<sup>(2)</sup> All voltage values are with respect to GND. For reliable operation the device should not be continuously operated at 7 V for more than t = 1000 h.

<sup>(3)</sup> Long-term high-temperature storage and/or extended use at maximum recommended operating conditions may result in a reduction of overall device life. See www.ti.com/ep\_quality for additional information on enhanced plastic packaging.





#### **ELECTRICAL CHARACTERISTICS**

Over recommended operating free-air temperature range, unless otherwise noted.

	PARAMETER	TEST CO	ONDITIONS	MIN	TYP	MAX	UNIT		
Vон	High-level output voltage (TPS3805 or	$V_{DD} = 1.5 \text{ V},$ $V_{DD} = 3.3 \text{ V},$ $V_{DD} = 6 \text{ V},$	$I_{OH} = -0.5 \text{ mA}$ $I_{OH} = -1 \text{ mA}$ $I_{OH} = -1.5 \text{ mA}$	0.8 x V <sub>DD</sub>			٧		
VOL	Low-level output voltage	$V_{DD} = 1.5 \text{ V},$ $V_{DD} = 3.3 \text{ V},$ $V_{DD} = 6 \text{ V},$	$I_{OL} = 1 \text{ mA}$ $I_{OL} = 2 \text{ mA}$ $I_{OL} = 3 \text{ mA}$			0.3	V		
	Power-up reset voltage(1)	VIT > 1.5 V, TA =	+25°C		0.8			V	
	Fower-up reset voltage(1)	VIT ≤ 1.5 V, TA =	+25°C		1			V	
	Managhar and an Inner the about	SENSE			1.2	1.226	1.244		
$\vee_{IT}$	Negative-going input threshold voltage <sup>(2)</sup>	TPS3803G15			1.379	1.4	1.421	V	
	voltagovi	TPS3805H33		3.004	3.05	3.096			
V.	I hantauania		1.2 V < V <sub>IT</sub> < 2.	5 V		15		\/	
V <sub>hys</sub>	Hysteresis		2.5 V < V <sub>IT</sub> < 3.	5 V		30		mV	
lį	Input current	SENSE			-25		25	nA	
IOH	High-level output current at RESET	Open-drain only	$V_{DD} = V_{IT} + 0.$	2V, V <sub>OH</sub> = V <sub>DD</sub>			300	nA	
		TPS3803-01				2	4		
		TPS3805, TPS3803G15	$V_{DD} = 3.3 V$ , output unconnected			3	5		
IDD	Supply current	TPS3803-01				2	4	μΑ	
		TPS3805, TPS3803G15	V <sub>DD</sub> = 6 V, output unconnected			4	6		
Cl	Input capacitance		$V_I = 0 V \text{ to } V_{DI}$	)		1		pF	

#### **TIMING REQUIREMENTS**

AT R<sub>L</sub> = 1 M $\Omega$ , C<sub>L</sub> = 50 PF, over recommended operating free-air temperature range.

	PARAMETER		TEST CONDITIONS	MIN	TYP	MAX	UNIT
Γ.	Dula a widdle	At V <sub>DD</sub>	V 4.05 mV V 0.05 mV				_
ľ	t <sub>w</sub> Pulse width	At SENSE	$V_{IH} = 1.05 \times V_{IT}, V_{IL} = 0.95 \times V_{IT}$	5.5			μs

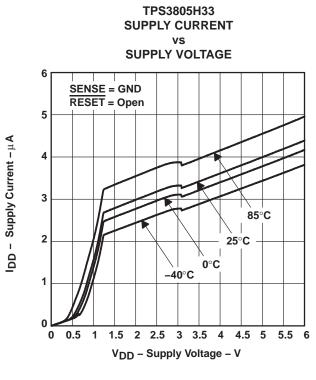
#### **SWITCHING CHARACTERISTICS**

AT R<sub>I</sub> = 1 M $\Omega$ , C<sub>I</sub> = 50 PF, over recommended operating free-air temperature range.

	, <u>L</u>	1 0	1 0				
	PARAMETER		TEST CONDITIONS	MIN	TYP	MAX	UNIT
tPHL	Propagation (delay) time, high-to-low-level output PhL PhL PhL Propagation (delay) time, SENSE to RESET de		V <sub>IH</sub> = 1.05 x V <sub>IT</sub> ,		5	100	
<sup>t</sup> PLH	Propagation (delay) time, low-to-high-level output	V <sub>DD</sub> to RESET delay SENSE to RESET delay	V <sub>IL</sub> = 0.95 × V <sub>IT</sub>		5	100	μs

<sup>(1)</sup> The lowest supply voltage at which  $\overline{\text{RESET}}$  (VOL(max) = 0.2 V, IOL = 50  $\mu$ A) becomes active.  $t_{\Gamma(\text{VDD})} \ge 15 \,\mu\text{s/V}$  (2) To ensure the best stability of the threshold voltage, place a bypass capacitor (ceramic, 0.1  $\mu$ F) near the supply terminals.





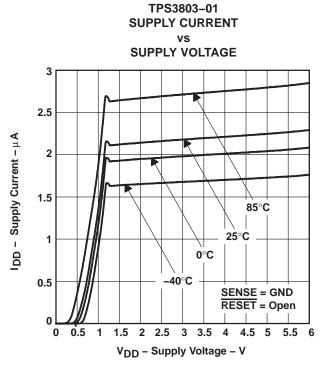
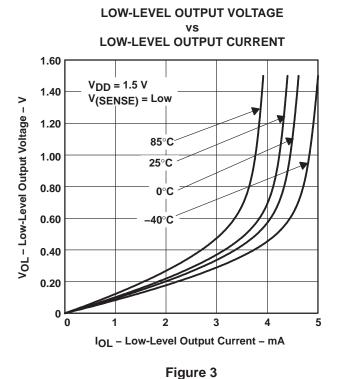


Figure 1





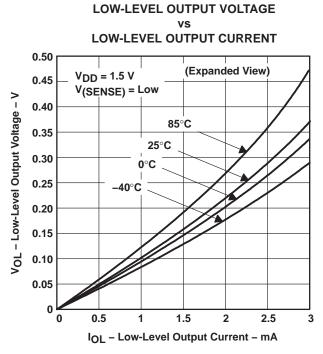


Figure 4



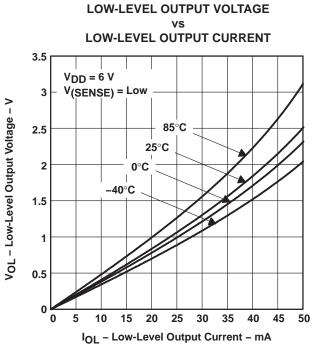


Figure 5

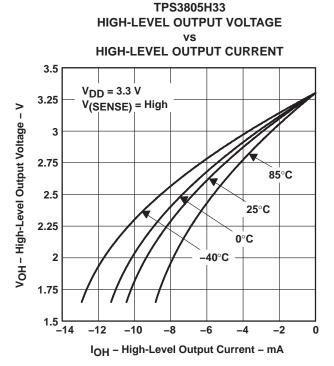


Figure 7

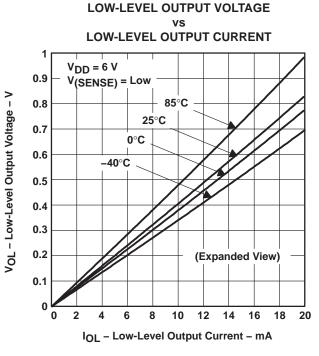


Figure 6

TPS3805H33

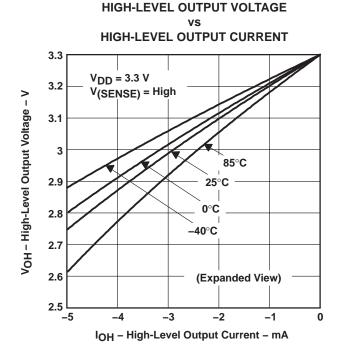


Figure 8



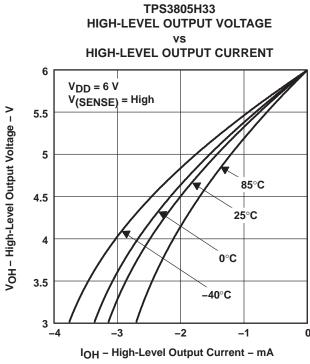
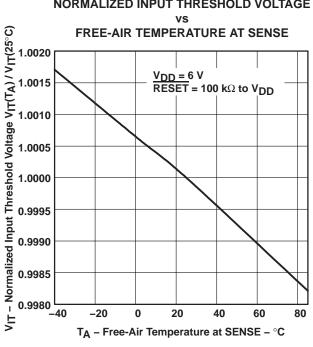
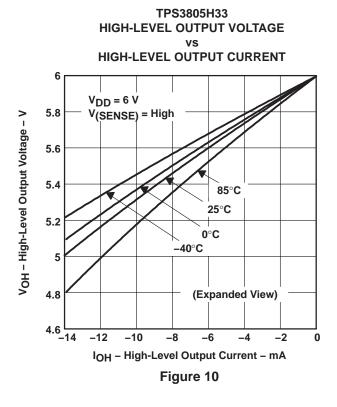
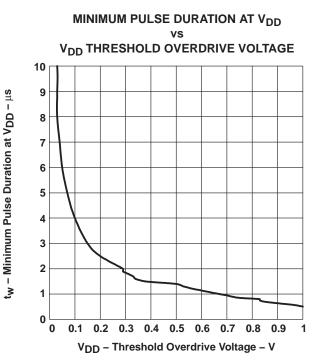


Figure 9 TPS3803-01 NORMALIZED INPUT THRESHOLD VOLTAGE











## MINIMUM PULSE DURATION AT SENSE vs SENSE THRESHOLD OVERDRIVE VOLTAGE

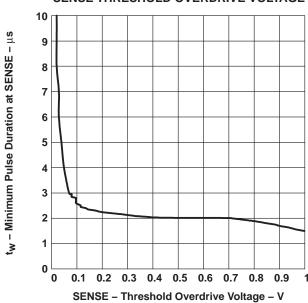


Figure 13

## **Revision History**

DATE	REV	PAGE	SECTION	DESCRIPTION
6/07	C	Front Page	_	Updated front page.
0/07	C	3	_	Functional block diagram change.

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.





22-Dec-2016

#### **PACKAGING INFORMATION**

Orderable Device	Status	Package Type	_	Pins	_	Eco Plan	Lead/Ball Finish	MSL Peak Temp	Op Temp (°C)	Device Marking	Samples
	(1)		Drawing		Qty	(2)	(6)	(3)		(4/5)	
TPS3803-01MDCKREP	ACTIVE	SC70	DCK	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-55 to 125	BAY	Samples
TPS3803-01QDCKREP	ACTIVE	SC70	DCK	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 125	AWH	Samples
TPS3803G15MDCKREP	ACTIVE	SC70	DCK	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAUAG	Level-1-260C-UNLIM	-55 to 125	ARH	Samples
TPS3803G15QDCKREP	ACTIVE	SC70	DCK	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	AXT	Samples
TPS3805H33MDCKREP	ACTIVE	SC70	DCK	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAUAG	Level-1-260C-UNLIM	-55 to 125	ARJ	Samples
TPS3805H33QDCKREP	ACTIVE	SC70	DCK	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	AWY	Samples
V62/04648-01XE	ACTIVE	SC70	DCK	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 125	AWH	Samples
V62/04648-02XE	ACTIVE	SC70	DCK	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	AXT	Samples
V62/04648-03XE	ACTIVE	SC70	DCK	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	AWY	Samples
V62/04648-04XE	ACTIVE	SC70	DCK	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-55 to 125	BAY	Samples
V62/04648-05XE	ACTIVE	SC70	DCK	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAUAG	Level-1-260C-UNLIM	-55 to 125	ARH	Samples
V62/04648-06XE	ACTIVE	SC70	DCK	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAUAG	Level-1-260C-UNLIM	-55 to 125	ARJ	Samples

<sup>(1)</sup> The marketing status values are defined as follows:

**ACTIVE:** Product device recommended for new designs.

**LIFEBUY:** TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

**TBD:** The Pb-Free/Green conversion plan has not been defined.

<sup>(2)</sup> Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.





22-Dec-2016

**Pb-Free** (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes. **Pb-Free** (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

- (3) MSL. Peak Temp. The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.
- (4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.
- (5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.
- (6) Lead/Ball Finish Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

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#### OTHER QUALIFIED VERSIONS OF TPS3803-01-EP, TPS3803-EP, TPS3803G15-EP, TPS3805H33-EP:

- Catalog: TPS3803-01, TPS3803, TPS3803G15, TPS3805H33
- Automotive: TPS3803-01-Q1, TPS3803-Q1, TPS3805H33-Q1

NOTE: Qualified Version Definitions:

- Catalog TI's standard catalog product
- Automotive Q100 devices qualified for high-reliability automotive applications targeting zero defects

**PACKAGE MATERIALS INFORMATION** 

www.ti.com 3-Aug-2017

#### TAPE AND REEL INFORMATION





	Dimension designed to accommodate the component width
	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

#### QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



#### \*All dimensions are nominal

Device	Package Type	Package Drawing		SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
TPS3803-01MDCKREP	SC70	DCK	5	3000	180.0	8.4	2.4	2.5	1.2	4.0	8.0	Q3
TPS3803-01QDCKREP	SC70	DCK	5	3000	180.0	8.4	2.4	2.5	1.2	4.0	8.0	Q3
TPS3803G15MDCKREP	SC70	DCK	5	3000	180.0	8.4	2.41	2.41	1.2	4.0	8.0	Q3
TPS3803G15QDCKREP	SC70	DCK	5	3000	180.0	8.4	2.41	2.41	1.2	4.0	8.0	Q3
TPS3805H33MDCKREP	SC70	DCK	5	3000	180.0	8.4	2.41	2.41	1.2	4.0	8.0	Q3
TPS3805H33QDCKREP	SC70	DCK	5	3000	180.0	8.4	2.41	2.41	1.2	4.0	8.0	Q3

www.ti.com 3-Aug-2017



\*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
TPS3803-01MDCKREP	SC70	DCK	5	3000	202.0	201.0	28.0
TPS3803-01QDCKREP	SC70	DCK	5	3000	202.0	201.0	28.0
TPS3803G15MDCKREP	SC70	DCK	5	3000	202.0	201.0	28.0
TPS3803G15QDCKREP	SC70	DCK	5	3000	202.0	201.0	28.0
TPS3805H33MDCKREP	SC70	DCK	5	3000	202.0	201.0	28.0
TPS3805H33QDCKREP	SC70	DCK	5	3000	202.0	201.0	28.0

## DCK (R-PDSO-G5)

## PLASTIC SMALL-OUTLINE PACKAGE



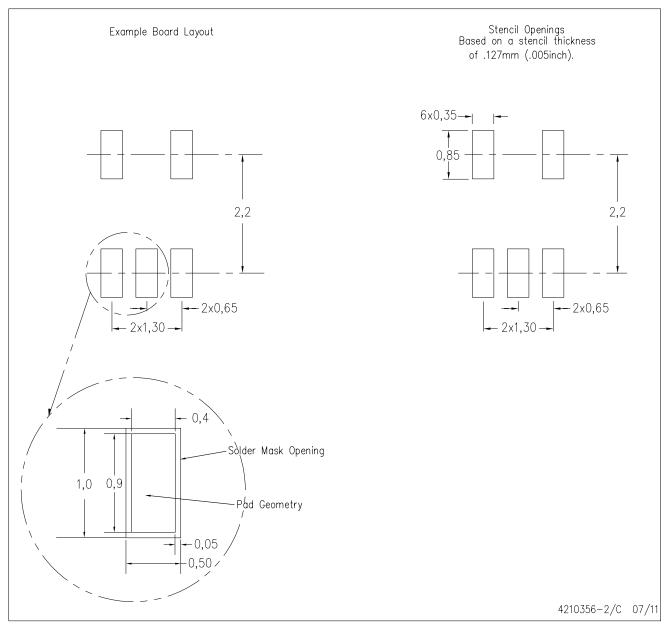
NOTES: A. All linear dimensions are in millimeters.

- B. This drawing is subject to change without notice.
- C. Body dimensions do not include mold flash or protrusion. Mold flash and protrusion shall not exceed 0.15 per side.
- D. Falls within JEDEC MO-203 variation AA.



## DCK (R-PDSO-G5)

## PLASTIC SMALL OUTLINE



NOTES:

- A. All linear dimensions are in millimeters.
- B. This drawing is subject to change without notice.
- C. Customers should place a note on the circuit board fabrication drawing not to alter the center solder mask defined pad.
- D. Publication IPC-7351 is recommended for alternate designs.
- E. Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Example stencil design based on a 50% volumetric metal load solder paste. Refer to IPC-7525 for other stencil recommendations.



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