

Demonstration Board for the SA57-IHZ

INTRODUCTION

The DB63 is designed to demonstrate the capabilities of the SA57 DC brush motor driver IC. This fully assembled demonstration allows the user to directly control the speed and direction of the motor. An onboard circuit controls the direction and provides four quadrant PWM signals to control the power outputs of the SA57. LEDs provide feedback for motor control status and fault indications. Provisions on the DB63 allow the user to bypass the onboard control circuit and directly interface with the SA57 motor driver.

The DB63 demonstrates proper layout techniques for the SA57 high current switching amplifier. The economical construction uses only a two-sided PCB and allows the SA57 to deliver peak currents of over 1kW.

THERMAL CONSIDERATIONS:

The SA57 is available in a surface mount package which can deliver peak power of over 1kW. This presents an obvious and significant thermal challenge. The DB63 offers a compact design which can deliver 17A peak current by using a patent pending mounting technique. Mounting the SA57 in an inverted fashion as shown in Figure 1 reduces the profile height of the assembly and provides a direct interface between the thermal tab of the PowerQuad package (package outline drawing HQ) and the small HS33 heatsink. The DB63 assembly can dissipate 7-9W in still air at 25°C ambient temperature, depending on the orientation of the heatsink fins. To use the DB63 in higher power applications, use a fan or mount a heatsink with larger thermal mass. Although the SA57 is rated for operation from -25 to +85°C, the other components on the DB63 are limited to 0 to 70°C ambient temperature.

CIRCUIT OPERATION

The DB63 control circuit receives power via two terminal block connections. The V_s connection supplies power to drive the motor and must be above the under-voltage lock-out threshold of the SA57, approximately 8.3V. The control circuit requires 12V for proper operation a regulator on the DB63 provides the 5V logic supply for the SA57. There are no special considerations for sequencing the two supplies.



PATENT PENDING

Figure 1 – Mounting Technique

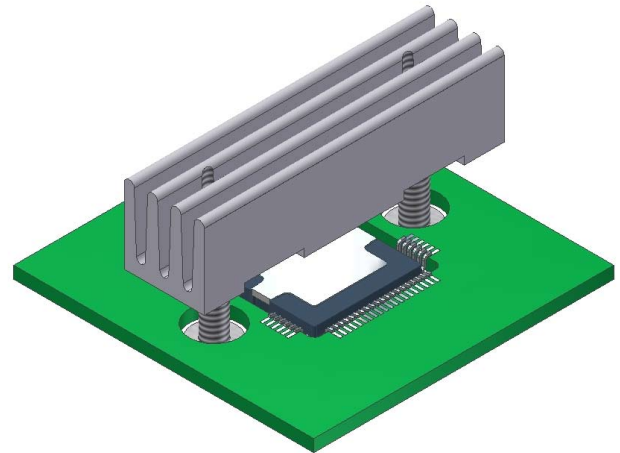


Figure 2 – User Control Features

Figure 2 shows the user control features of the DB63. The PWM duty cycle is controlled with the potentiometer (1 in figure 2). The power LED (2) will illuminate when the 12V supply is connected. The DB63 will power up with the SA57 disabled. The enable button (3) will toggle the SA57 on and off with the LED (4) illuminating to indicate the enable status. Direction of the motor is similarly controlled with the button (5) and is indicated by the LED (6).

The DB63 monitors the Temperature warning status pin of the SA57. If this pin goes high an LED (7) illuminates and the enable circuit is forced to a disable status. The temperature LED is not latched and may stay illuminated only briefly while the temperature of the SA57 is above 135°C. The temperature decrease rapidly via the heatsink once the SA57 is disabled.

The SA57 current limit feature is set to limit at approximately 15A to provide a demonstration of the full capabilities of the SA57. An LED (8) will illuminate if the SA57 cycle-by-cycle current limit circuit engages. The thermal and current limit features are robust, but will not protect the SA57 in all circumstances. The user must consider the worst case thermal and power dissipation conditions.

Push-button switches 1 and 2 trigger latches (U4) for direction and enable control, respectively. Diodes D7 & D8 and resistors R24, 25, 29 & 30 provide a means of bypassing the DB63 control circuit. The 5V regulator, U3, provides 5V to the SA57, the latches and the status LEDs.

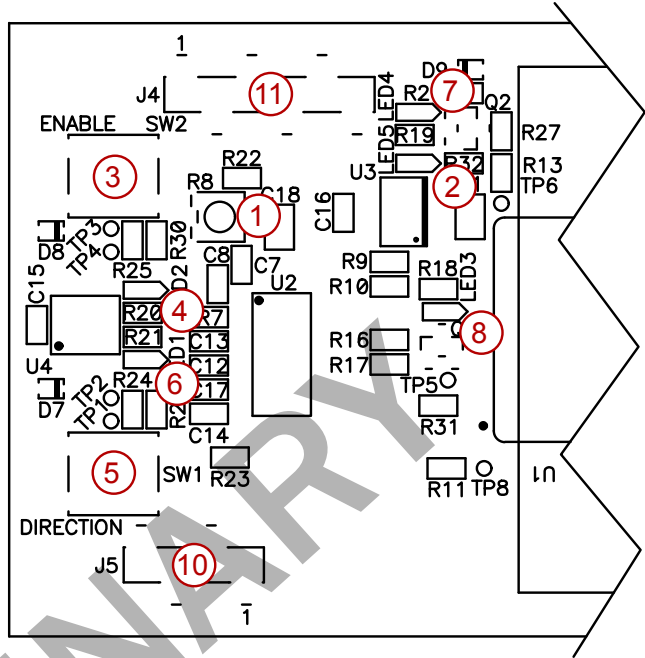
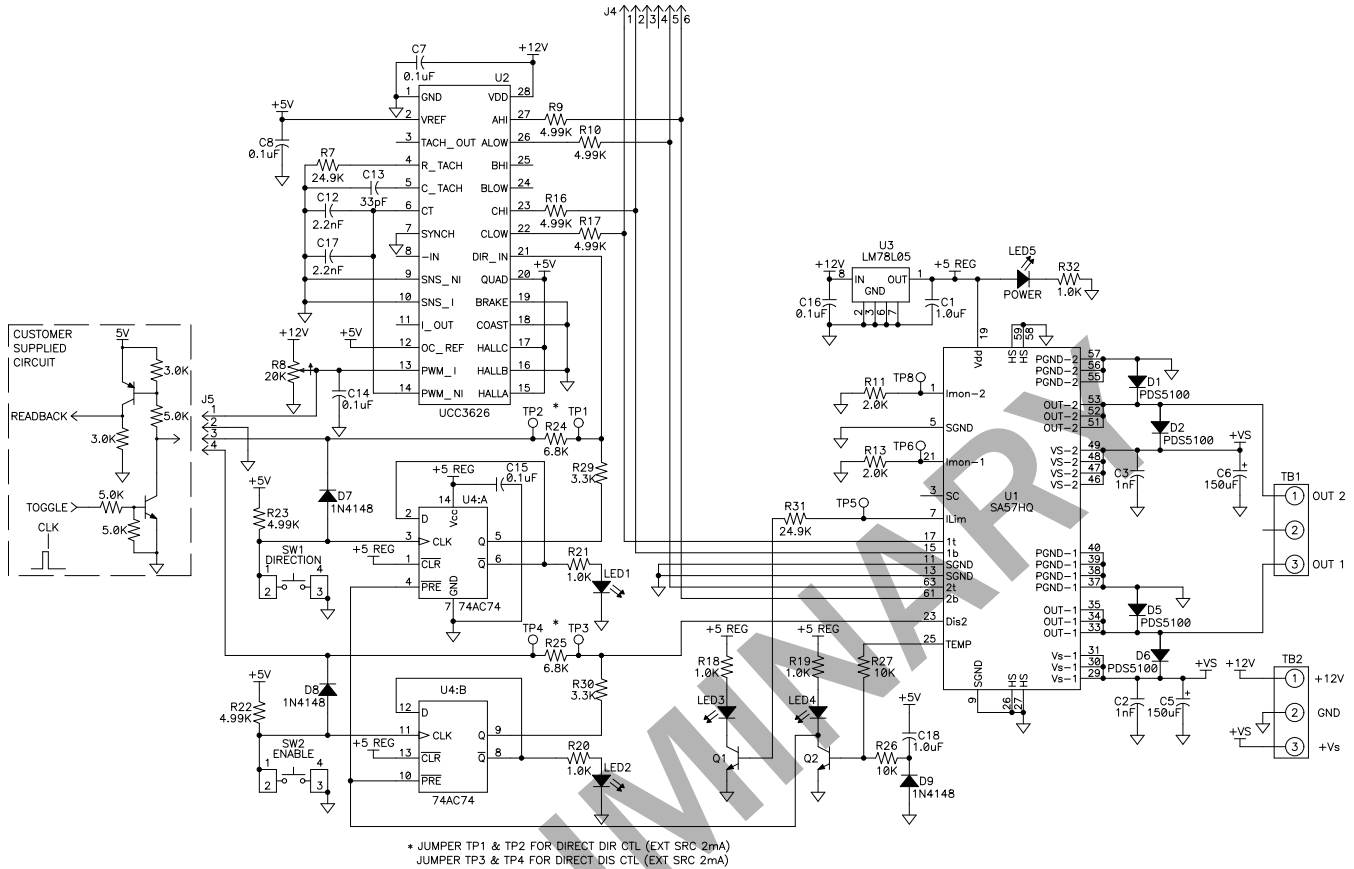


Figure 3 – Schematic



ENHANCING & BYPASSING THE DB63 CONTROL CIRCUIT:

Connector J5 allows the user to bypass many of the manual control features of the DB63. A signal generator can control the duty cycle with a 2.5 to 7.5V signal, overriding the control potentiometer. A rising 5V edge on pin 3 or 4 of connector J5 will toggle the Direction or Enable latches, respectively. By jumping resistors R24 & R25, the latches are bypassed completely and the logic signals on pins 3 & 4 will directly control the direction and enable functions of the DB63. With these resistors jumped, the direction and enable LEDs will not represent the states of the DB63 and the pushbuttons will have no effect on the operation. The Temperature disable feature of the DB63 will also not function, although the LED will continue to provide over-temperature status.

Connector J4 is connected directly to the PWM input pins of the SA57. This connector may be used to monitor the signals or to bypass the control IC on the DB63. The enable function is not controlled via these pins, although pulling all four input pins low provides the same effect. The Enable pushbutton and the connection via J5 are also effective as previously described. The circuit shown in figure 3 in the dashed box is a simple circuit that allows the user to monitor and control the enable or direction status remotely. Either feature can be toggled on the falling edge of the signal at the node labeled TOGGLE.

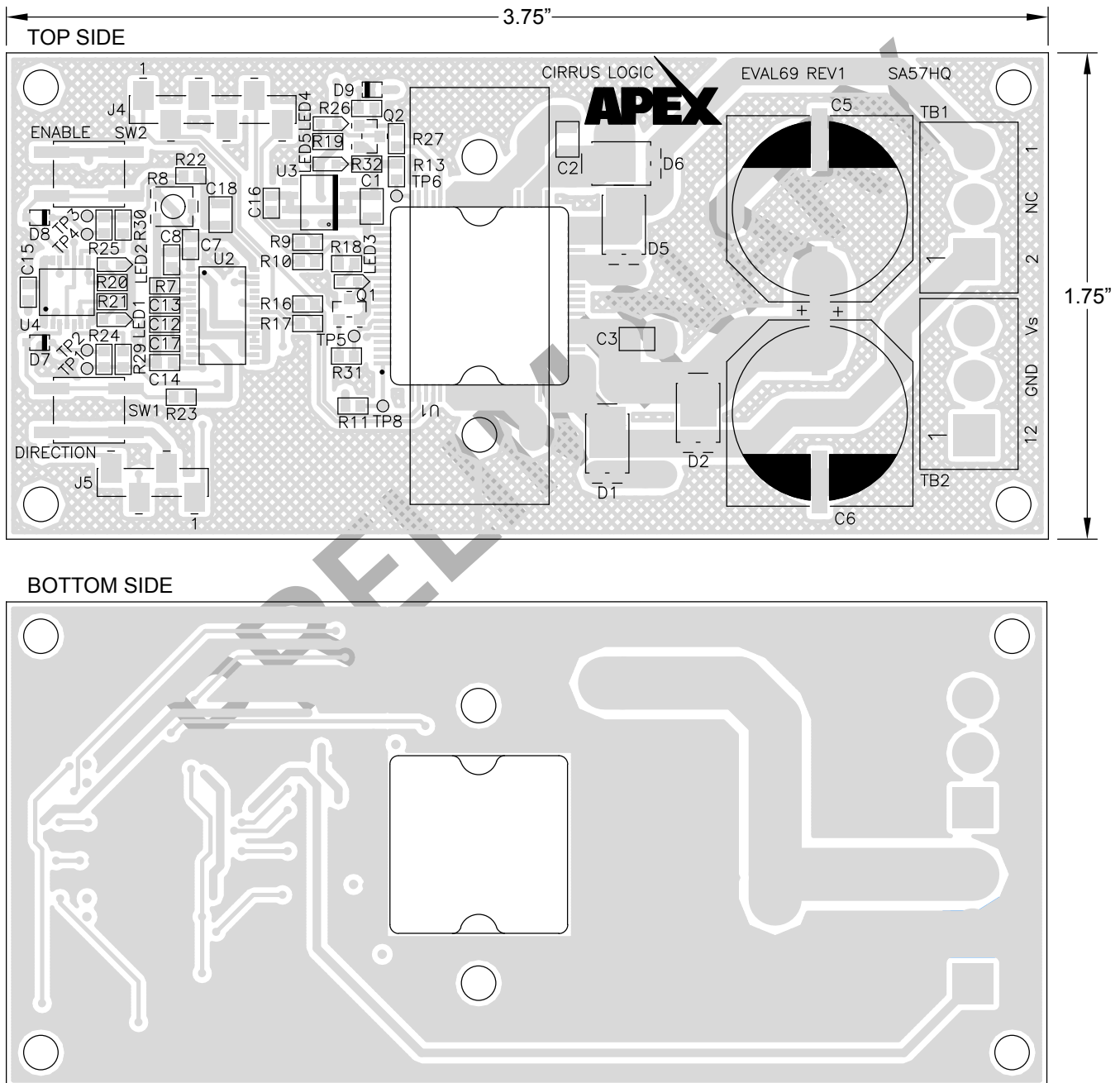
LAYOUT CONSIDERATIONS

A simple two layer construction is sufficient because of the convenient pinout of the SA57 PowerQuad package. Input signals are routed into one side of the package and high power output signals are routed from the other side in 2 ounce copper. This eliminates the need to route control signals near motor connections where noise may corrupt the signals. Filling top and bottom layers with copper reduces inductive coupling from the high current outputs. 1nF capacitors with excellent high frequency characteristics bypass the Vs motor supplies on each phase. Two 150µF electrolytic capacitors provide a local, low inductance source to accommodate surge currents up to 17A.

Six 100V Schottky diodes conduct the commutation current via low forward voltage paths which reduces the power dissipation in the SA57. These diodes are rated for 5A continuous and are mounted close to the SA57 to reduce inductance in the commutating current loop. For applications with continuous currents less than 5A, the Schottky diodes may not be necessary if the higher forward voltage internal body diodes and the associated power dissipation are manageable during commutation cycles.

Figure 4 shows the top and bottom layouts of the DB63. Gerber files for the circuit board are available upon request.

Figure 4 – PCB Layout (not to scale)



BILL OF MATERIALS

Designation	Description	P/N
C1,C18	CAP, 1.0uF, 16V	Kemet, C0805C105K4RAC
C13	CAP, 33pF	Kemet, C0603C330J5GACTU
C2,C3	CAP, 1.0nF,100V	Kemet,C0805C102J1GACTU
C5,C6	CAP, 150uF, 100V	Panasonic, EEVFK2A151M
C7,C8,C14,C15,C16	CAP, 0.1uF,16V	GRM188F51C104ZA01D
C12,C17	CAP, 2.2nF,50V	GRM188R71H222KA01D
D1,D2,D3,D5,D6	Diode, 5A Schottky	Diodes Inc., DS5100
D7,D8,D9	IN4148	Vishay, 1N4148WS-V-GS08
J4	Conn, 6 pin .100 ctrs	Samtec, TSM-106-01-T-SV
J5	Conn, 4 pin .100 ctrs	Samtec, TSM-104-01-T-SV
LED1-5	LED, Red	Lite-On, LTST-C190CKT
Q1,Q2	XTR, NPN	MMBT3904
R11,R12,R13	RES 2.0K, 1%	Vishay,CRCW06032K00FKEA
R24,R25	RES, 6.8K	Vishay,CRCW06036K80FKEA
R26,R27	RES, 10K	Vishay,CRCW060310K0FKEA
R29,R30	RES, 3.3K	Vishay,CRCW06033K30FKEA
R18,R19,R20,R21,R32	RES, 1.0K, 5%	RK73B1JTDD102J
R7,R31	RES, 24.9K,1/10W, 1%	RK73H1JTDD2492F
R8	POT, 20K	muRata, PVG3A203C01
R9,R10,R16,R17,R22,R23	RES, 4.99K	CRCW06034K99FKEA
SW1,SW2	Pushbutton, SPST-NO	Panasonic, EVQ-Q2B01W
TB1,TB2	Terminal Block, 3 pin, 5mm	On Shore Tech, OSTTA034163
Thermal Grease	Heatsink compound	N/A
U1	SA57-IHZ	Cirrus Logic, SA57-IHZ
U2	UCC3626	Texas Instruments, UCC3626PW
U3	LM78L05	National Semiconductor, LM78L05ACMX/NOPB
U4	74AC74	Texas Instruments, SN74AC74PW
	Heatsink	Cirrus Logic, HS33
	PCB, 1.75" x 3.75"	Cirrus Logic,EVAL69
	Screw, #4-40 x 1/4"	N/A

ORDERING INFORMATION

DB63 Demonstration Board includes one populated EVAL69 PCB and one SA57-IHZ sample.

CONTACTING CIRRUS LOGIC SUPPORT

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