

QUICK SELECTOR GUIDES — SILICON HIGH-SPEED SWITCHING AND GENERAL PURPOSE TRANSISTORS

The following two tables categorize the silicon devices included in this section into two classifications — those intended for general-purpose switching and amplifier applications, and those recommended primarily for high-speed saturated switching purposes.

Only the preferred devices — those that merit first consideration for new designs — are listed. In each table, the devices are grouped in voltage and current ranges. The voltage given is the minimum collector-emitter breakdown voltage (BV_{CEO}). The current range columns represent operating current values for which optimum current gain (h_{FE}) and/or collector-emitter saturation voltage ($V_{CE(sat)}$) are specified in the data sheets.

SATURATED SWITCHING TRANSISTORS (SILICON) Current versus Voltage

BV _{CEO} Min Volts	OPTIMUM COLLECTOR CURRENT											
	0 to 10 mA		10 mA to 100 mA		100 mA to 500 mA		500 mA to 1.0 A		1.0 A to 3.0 A		3.0 A to 5.0 A	
	NPN	PNP	NPN	PNP	NPN	PNP	NPN	PNP	NPN	PNP	NPN	PNP
0 ↓ 19	2N3010 2N3493 MM709 MM1748	2N2894 2N3546 2N4411	2N2369A 2N3009 2N3010 2N3011 2N3013 2N3210 2N3211	2N2894 2N3546	2N3009 2N3013 2N3510 2N3511 2N3647 2N3648		2N3303		2N3303			
20 ↓ 29	2N702 2N703		2N2501 2N3014 2N3227 2N3508 2N3509		2N2476 2N2477 2N2501 2N2847 2N2848							
30 ↓ 39			2N2537 2N2538 2N2539 2N2540		2N2537 2N2538 2N2539 2N2540 2N2845 2N2846 2N3015 2N3724 2N4013 2N4046		2N3252 2N3724 2N3734 2N3736 2N4013 2N4046		2N3734 2N3736			
40 ↓ 59			2N3725 2N4014		2N3725 2N4014 2N4047	2N3467 2N3468	2N3253 2N3444 2N3725 2N3735 2N3737 2N4014 2N4047	2N3467 2N3468 2N3762 2N3764	2N3444 2N3735 2N3737	2N3762 2N3764	2N3506 2N3507	
60 79								2N3763 2N3765		2N3763 2N3765		

2N3724(SILICON)

2N3725

2N4013

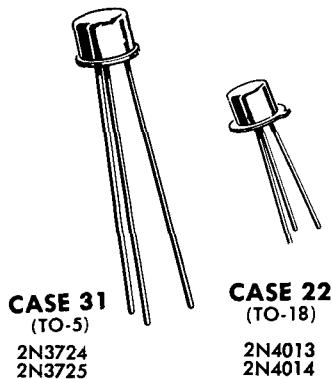
2N4014

$V_{CE0} = 30$ to 50 V

$I_C = 1$ A

$f_T = 300$ MHz

NPN silicon annular low-power transistors primarily designed for high-speed saturated switching applications.



MAXIMUM RATINGS

Rating	Symbol	2N4013 2N3724	2N4014 2N3725	Unit
Collector-Emitter Voltage	V_{CE0}	30	50	Vdc
Collector-Base Voltage	V_{CB}	50	80	Vdc
Emitter-Base Voltage	V_{EB}	6.0		Vdc
Collector Current	I_C	1.0		Adc
Total Device Dissipation @ $T_A = 25^\circ\text{C}$ Derate above 25°C	P_D	800 4.6	360 2.06	mW mW/ $^\circ\text{C}$
Total Device Dissipation @ $T_C = 25^\circ\text{C}$ Derate above 25°C	P_D	3.5 20	1.2 6.85	Watts mW/ $^\circ\text{C}$
Operating and Storage Junction Temperature Range	T_J, T_{stg}	-65 to +200		$^\circ\text{C}$

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
----------------	--------	-----	-----	------

OFF CHARACTERISTICS

Collector-Emitter Breakdown Voltage* ($I_C = 10$ mAdc, $I_B = 0$)	2N3724, 2N4013 2N3725, 2N4014	BV_{CE0}^*	30 50	-	Vdc
Collector-Emitter Breakdown Voltage ($I_C = 10$ μ Adc, $V_{BE} = 0$)	2N3724, 2N4013 2N3725, 2N4014	BV_{CES}	50 80	-	Vdc
Collector-Base Breakdown Voltage ($I_C = 10$ μ Adc, $I_E = 0$)	2N3724, 2N4013 2N3725, 2N4014	BV_{CBO}	50 80	-	Vdc
Emitter-Base Breakdown Voltage ($I_E = 10$ μ Adc, $I_C = 0$)		BV_{EBO}	6.0	-	Vdc
Collector Cutoff Current ($V_{CE} = 50$ Vdc, $V_{BE} = 0$) ($V_{CE} = 80$ Vdc, $V_{BE} = 0$)	2N3724, 2N4013 2N3725, 2N4014	I_{CES}	- -	10 10	μ Adc
Collector Cutoff Current ($V_{CB} = 40$ Vdc, $I_E = 0$) ($V_{CB} = 40$ Vdc, $I_E = 0$, $T_A = 100^\circ\text{C}$) ($V_{CB} = 60$ Vdc, $I_E = 0$) ($V_{CB} = 60$ Vdc, $I_E = 0$, $T_A = 100^\circ\text{C}$)	2N3724, 2N4013 2N3724, 2N4013 2N3725, 2N4014 2N3725, 2N4014	I_{CBO}	- - - -	1.7 120 1.7 120	μ Adc
Base Current ($V_{CE} = 50$ Vdc, $V_{BE} = 0$) ($V_{CE} = 80$ Vdc, $V_{BE} = 0$)	2N3724, 2N4013 2N3725, 2N4014	I_B	- -	10 10	μ Adc

ON CHARACTERISTICS

DC Current Gain* ($I_C = 10$ mAdc, $V_{CE} = 1.0$ Vdc) ($I_C = 100$ mAdc, $V_{CE} = 1.0$ Vdc) ($I_C = 300$ mAdc, $V_{CE} = 1.0$ Vdc) ($I_C = 500$ mAdc, $V_{CE} = 1.0$ Vdc) ($I_C = 800$ mAdc, $V_{CE} = 2.0$ Vdc) ($I_C = 1.0$ Adc, $V_{CE} = 5.0$ Vdc) ($I_C = 100$ mAdc, $V_{CE} = 1.0$ Vdc, $T_A = -55^\circ\text{C}$) ($I_C = 500$ mAdc, $V_{CE} = 1.0$ Vdc, $T_A = -55^\circ\text{C}$)	2N3724, 2N4013 2N3725, 2N4014 2N3724, 2N4013 2N3725, 2N4014 2N3724, 2N4013	h_{FE}^*	30 60 40 35 25 20 30 25 30 20	- 150 - - - - - - - -	-
--	--	------------	--	--	---

2N3724, 2N3725 — 2N4013, 2N4014 (continued)

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
----------------	--------	-----	-----	------

ON CHARACTERISTICS (continued)

Collector-Emitter Saturation Voltage* ($I_C = 10\text{ mAdc}$, $I_B = 1.0\text{ mAdc}$)	2N3724, 2N4013 2N3725, 2N4014 2N3724, 2N4013 2N3725, 2N4014 2N3724, 2N4013 2N3725, 2N4014 2N3724, 2N4013 2N3725, 2N4014	-	0.25	Vdc
($I_C = 100\text{ mAdc}$, $I_B = 10\text{ mAdc}$)			0.20	
($I_C = 300\text{ mAdc}$, $I_B = 30\text{ mAdc}$)			0.26	
($I_C = 500\text{ mAdc}$, $I_B = 50\text{ mAdc}$)			0.32	
($I_C = 800\text{ mAdc}$, $I_B = 80\text{ mAdc}$)			0.40	
($I_C = 1.0\text{ Adc}$, $I_B = 100\text{ mAdc}$)			0.42	
($I_C = 1.0\text{ Adc}$, $I_B = 100\text{ mAdc}$)			0.52	
Base-Emitter Saturation Voltage* ($I_C = 10\text{ mAdc}$, $I_B = 1.0\text{ mAdc}$)	2N3724, 2N4013 2N3725, 2N4014 2N3724, 2N4013 2N3725, 2N4014 2N3724, 2N4013 2N3725, 2N4014 2N3724, 2N4013 2N3725, 2N4014	-	0.76	Vdc
($I_C = 100\text{ mAdc}$, $I_B = 10\text{ mAdc}$)			0.86	
($I_C = 300\text{ mAdc}$, $I_B = 30\text{ mAdc}$)			1.1	
($I_C = 500\text{ mAdc}$, $I_B = 50\text{ mAdc}$)			1.2	
($I_C = 800\text{ mAdc}$, $I_B = 80\text{ mAdc}$)			1.5	
($I_C = 1.0\text{ Adc}$, $I_B = 100\text{ mAdc}$)			1.7	
($I_C = 1.0\text{ Adc}$, $I_B = 100\text{ mAdc}$)			0.9	

SMALL-SIGNAL CHARACTERISTICS

Current-Gain-Bandwidth Product ($I_C = 50\text{ mAdc}$, $V_{CE} = 10\text{ Vdc}$, $f = 100\text{ MHz}$)	f_T	300	-	MHz
Output Capacitance ($V_{CB} = 10\text{ Vdc}$, $I_E = 0$, $f = 140\text{ kHz}$)	C_{ob}	-	12	pF
		-	10	
Input Capacitance ($V_{BE} = 0.5\text{ Vdc}$, $I_C = 0$, $f = 140\text{ kHz}$)	C_{ib}	-	55	pF

SWITCHING CHARACTERISTICS

Turn-On Time	$(V_{CC} = 30\text{ Vdc}$, $V_{BE(off)} = 3.8\text{ Vdc}$, $I_C = 500\text{ mAdc}$, $I_{B1} = 50\text{ mAdc}$) (See Figure 1)	t_{on}	-	35	ns
Delay Time		t_d	-	10	ns
Rise Time		t_r	-	30	ns
Turn-Off Time	$(V_{CC} = 30\text{ Vdc}$, $I_C = 500\text{ mAdc}$, $I_{B1} = I_{B2} = 50\text{ mAdc}$) (See Figure 1)	t_{off}	-	60	ns
Storage Time		t_s	-	50	ns
Fall Time		t_f	-	25	ns
				30	ns

* Pulse Test: Pulse Width = 300 μs , Duty Cycle = 1.0%.

FIGURE 1 — SWITCHING TIMES TEST CIRCUIT

