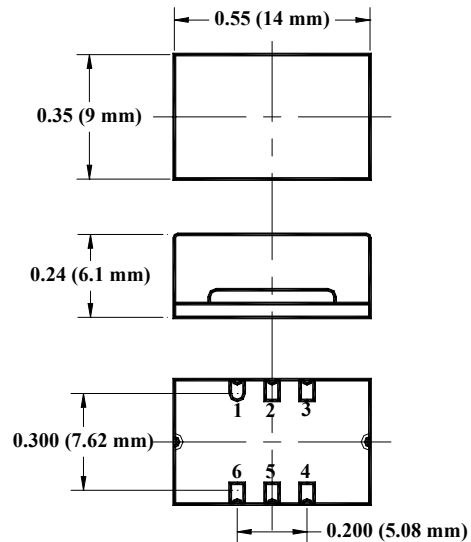
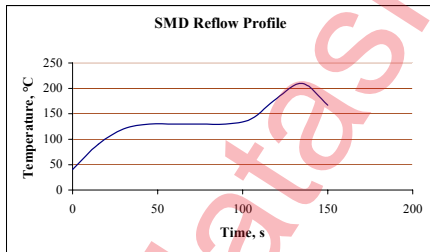


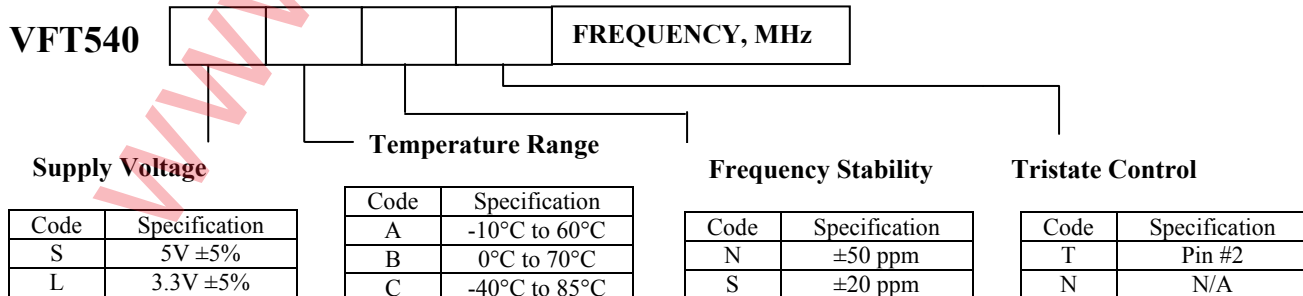
Product Data Sheet

Features

- Small, Low Profile SMD Package
- Very Low Phase Jitter and Phase Noise
- Excellent Frequency Stability
- Low Aging, Vacuum sealed Crystal
- SONET/SDH stability available
- High Frequency, no multiplication



Creating a Part Number



VFT540 Series SMD CLOCK

Specifications

Parameter	Symb	Condition	Min	Typ	Max	Unit	Note
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Absolute Maximum Ratings

Input Break Down Voltage	V _{cc}		-0.5		7.0	V	
Storage temp.	T _s		-40		125	°C	
Contr. Voltage	V _c		-1		9	V	

Electrical

Frequency Range	F		8		250	MHz	
Input Voltage	V _{cc}		3.135 4.75	3.30 5.0	3.465 5.25	V	L S
Input Current	I _{cc}	No Load			50	mA	@ 155 MHz
Frequency Stab.	ΔF/F	Overall					See chart
Frequency Stability for SONET	ΔF/F	vs. Temperature vs. V _{cc} aging Overall		±10 ±1 ±1		ppm ppm/V ppm/year ppm	First Year @V _c = 1.65 V
Load		15 pF max					
Duty cycle		@50%	45	50	55	%	
Rise/Fall time	T _r /T _f	20 to 80 %		2		ns	
Logic "1" level	V _{oh}		0.9V _{cc}			V	
Logic "0" level	V _{ol}				0.1V _{cc}	V	
Start up time	T _s			2	10	ms	
Phase jitter		1σ		0.4	1	ps	f _j >100 Hz
SSB Phase Noise		@10 Hz @100 Hz @1 KHz @10 KHz @100 KHz		-70 -100 -130 -150 -160		dBc/Hz	@155 MHz
Tristate Function	Input HIGH (>2.5V), or floating: ACTIVE Input LOW (<0.5V): INFINITE IMPEDANCE						
Enable/disable Time	T _e /T _d				100	ns	

Environmental and Mechanical

Operating temp. range	0°C to 70°C, -40°C to 85°C
Mechanical Shock	Per MIL-STD-202, Method 213, Cond. E
Thermal Shock	Per MIL-STD-883, Method 1011, Cond. A
Vibration	Per MIL-STD-883, Method 2007, Cond. A
Soldering Conditions	230°C for 90s Max
Hermetic Seal	Leak rate less than 5x10 ⁻⁸ atm.cc/s of helium (crystal only)

Electrical Connections

Pin Out	Pin #1- N/C Pin #2 – Tristate control (code “T”), or N/C, (code “N”) Pin #3 - Case, Gnd Pin #4 - Output Pin #5 – N/C Pin #6 - V _{cc}
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