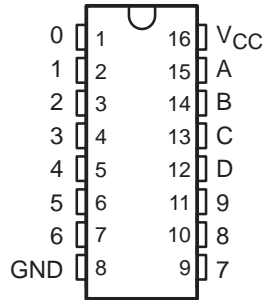


# SN54HC42, SN74HC42 4-LINE TO 10-LINE DECODERS (1 of 10)

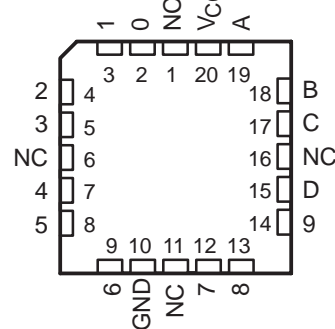
SCLS091D – DECEMBER 1982 – REVISED SEPTEMBER 2003

- Wide Operating Voltage Range of 2 V to 6 V
- Outputs Can Drive Up To 10 LSTTL Loads
- Low Power Consumption, 80- $\mu$ A Max  $I_{CC}$
- Typical  $t_{pd} = 14$  ns
- $\pm 4$ -mA Output Drive at 5 V
- Low Input Current of 1  $\mu$ A Max
- Full Decoding of Input Logic
- All Outputs Are High for Invalid BCD Conditions
- Also for Applications as 3-Line to 8-Line Decoders

SN54HC42 . . . J OR W PACKAGE  
SN74HC42 . . . D, N, OR NS PACKAGE  
(TOP VIEW)



SN54HC42 . . . FK PACKAGE  
(TOP VIEW)



NC – No internal connection

## description/ordering information

These decimal decoders consist of eight inverters and ten 4-input NAND gates. The inverters are connected in pairs to make BCD input data available for decoding by the NAND gates. Full decoding of valid input logic ensures that all inputs remain off for all invalid input conditions.

## ORDERING INFORMATION

TA	PACKAGE†		ORDERABLE PART NUMBER	TOP-SIDE MARKING
-40°C to 85°C	PDIP – N	Tube of 25	SN74HC42N	SN74HC42N
	SOIC – D	Tube of 40	SN74HC42D	HC42
		Reel of 2500	SN74HC42DR	
		Reel of 250	SN74HC42DT	
	SOP – NS	Reel of 2000	SN74HC42NSR	HC42
-55°C to 125°C	CDIP – J	Tube of 25	SNJ54HC42J	SNJ54HC42J
	CFP – W	Tube of 150	SNJ54HC42W	SNJ54HC42W
	LCCC – FK	Tube of 55	SNJ54HC42FK	SNJ54HC42FK

† Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at [www.ti.com/sc/package](http://www.ti.com/sc/package).



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PRODUCTION DATA information is current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.

 **TEXAS  
INSTRUMENTS**

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On products compliant to MIL-PRF-38535, all parameters are tested unless otherwise noted. On all other products, production processing does not necessarily include testing of all parameters.

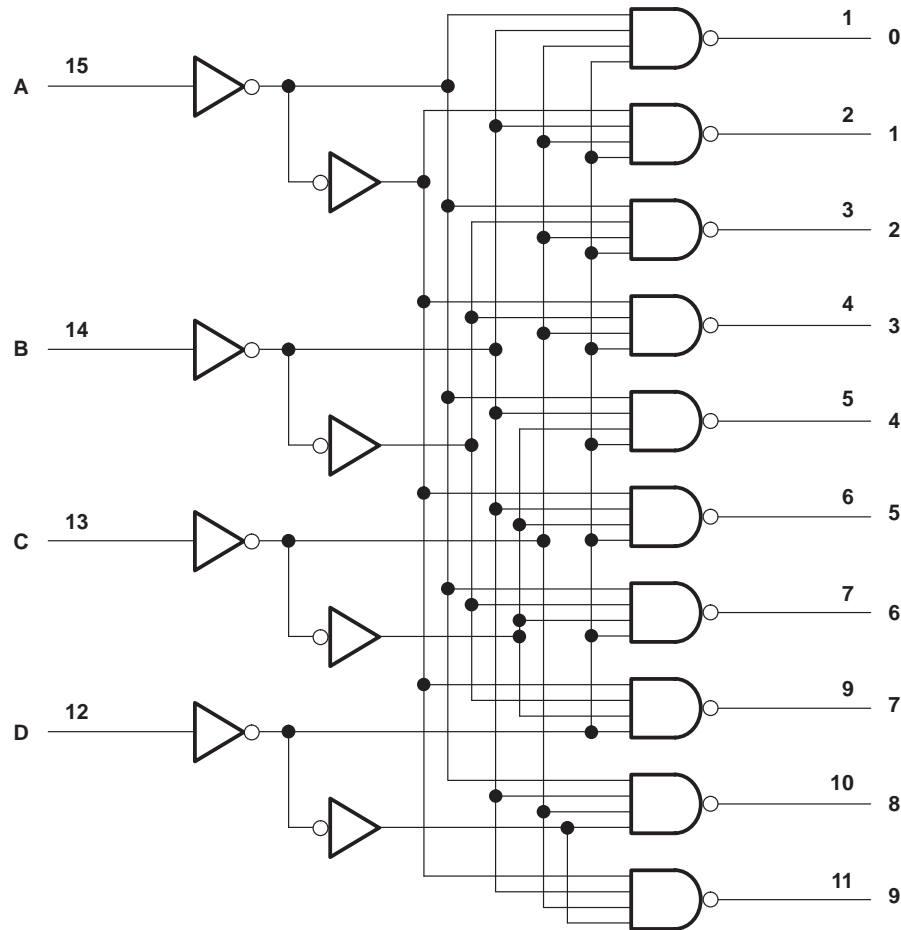
**SN54HC42, SN74HC42**  
**4-LINE TO 10-LINE DECODERS (1 of 10)**

SCLS091D – DECEMBER 1982 – REVISED SEPTEMBER 2003

**FUNCTION TABLE**

NO.	INPUTS				OUTPUTS									
	D	C	B	A	0	1	2	3	4	5	6	7	8	9
0	L	L	L	L	L	H	H	H	H	H	H	H	H	H
1	L	L	L	H	H	L	H	H	H	H	H	H	H	H
2	L	L	H	L	H	H	L	H	H	H	H	H	H	H
3	L	L	H	H	H	H	H	L	H	H	H	H	H	H
4	L	H	L	L	H	H	H	H	L	H	H	H	H	H
5	L	H	L	H	H	H	H	H	H	L	H	H	H	H
6	L	H	H	L	H	H	H	H	H	H	L	H	H	H
7	L	H	H	H	H	H	H	H	H	H	H	L	H	H
8	H	L	L	L	H	H	H	H	H	H	H	H	L	H
9	H	L	L	H	H	H	H	H	H	H	H	H	H	L
Invalid	H	L	H	L	H	H	H	H	H	H	H	H	H	H
	H	L	H	H	H	H	H	H	H	H	H	H	H	H
	H	H	L	L	H	H	H	H	H	H	H	H	H	H
	H	H	L	H	H	H	H	H	H	H	H	H	H	H
	H	H	H	L	H	H	H	H	H	H	H	H	H	H
	H	H	H	H	H	H	H	H	H	H	H	H	H	H

logic diagram (positive logic)



Pin numbers shown are for the D, J, N, NS, and W packages.

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

Supply voltage range, $V_{CC}$ .....	-0.5 V to 7 V
Input clamp current, $I_{IK}$ ( $V_I < 0$ or $V_I > V_{CC}$ ) (see Note 1) .....	$\pm 20$ mA
Output clamp current, $I_{OK}$ ( $V_O < 0$ or $V_O > V_{CC}$ ) (see Note 1) .....	$\pm 20$ mA
Continuous output current, $I_O$ ( $V_O = 0$ to $V_{CC}$ ) .....	$\pm 25$ mA
Continuous current through $V_{CC}$ or GND .....	$\pm 50$ mA
Package thermal impedance, $\theta_{JA}$ (see Note 2): D package .....	73°C/W
N package .....	67°C/W
NS package .....	64°C/W
Storage temperature range, $T_{stg}$ .....	-65°C to 150°C

† Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

- NOTES: 1. The input and output voltage ratings may be exceeded if the input and output current ratings are observed.  
2. The package thermal impedance is calculated in accordance with JESD 51-7.

# SN54HC42, SN74HC42

## 4-LINE TO 10-LINE DECODERS (1 of 10)

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### recommended operating conditions (see Note 3)

		SN54HC42			SN74HC42			UNIT
		MIN	NOM	MAX	MIN	NOM	MAX	
$V_{CC}$	Supply voltage	2	5	6	2	5	6	V
$V_{IH}$	High-level input voltage	$V_{CC} = 2\text{ V}$		1.5	1.5		V	
		$V_{CC} = 4.5\text{ V}$		3.15	3.15			
		$V_{CC} = 6\text{ V}$		4.2	4.2			
$V_{IL}$	Low-level input voltage	$V_{CC} = 2\text{ V}$			0.5	0.5	V	
		$V_{CC} = 4.5\text{ V}$			1.35	1.35		
		$V_{CC} = 6\text{ V}$			1.8	1.8		
$V_I$	Input voltage	0		$V_{CC}$	0	$V_{CC}$	V	
$V_O$	Output voltage	0		$V_{CC}$	0	$V_{CC}$	V	
$\Delta t/\Delta v$	Input transition rise/fall time	$V_{CC} = 2\text{ V}$			1000	1000	ns	
		$V_{CC} = 4.5\text{ V}$			500	500		
		$V_{CC} = 6\text{ V}$			400	400		
$T_A$	Operating free-air temperature	-55		125	-40	85	°C	

NOTE 3: All unused inputs of the device must be held at  $V_{CC}$  or GND to ensure proper device operation. Refer to the TI application report, *Implications of Slow or Floating CMOS Inputs*, literature number SCBA004.

### electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS		$V_{CC}$	$T_A = 25^\circ\text{C}$			SN54HC42		SN74HC42		UNIT
				MIN	TYP	MAX	MIN	MAX	MIN	MAX	
$V_{OH}$	$V_I = V_{IH}$ or $V_{IL}$	$I_{OH} = -20\ \mu\text{A}$	2 V	1.9	1.998		1.9	1.9	V		
			4.5 V	4.4	4.499		4.4	4.4			
			6 V	5.9	5.999		5.9	5.9			
		$I_{OH} = -4\ \text{mA}$	4.5 V	3.98	4.3		3.7	3.84			
		$I_{OH} = -5.2\ \text{mA}$	6 V	5.48	5.8		5.2	5.34			
$V_{OL}$	$V_I = V_{IH}$ or $V_{IL}$	$I_{OL} = 20\ \mu\text{A}$	2 V		0.002	0.1		0.1	0.1	V	
			4.5 V		0.001	0.1		0.1	0.1		
			6 V		0.001	0.1		0.1	0.1		
		$I_{OL} = 4\ \text{mA}$	4.5 V		0.17	0.26		0.4	0.33		
		$I_{OL} = 5.2\ \text{mA}$	6 V		0.15	0.26		0.4	0.33		
$I_I$	$V_I = V_{CC}$ or 0		6 V		$\pm 0.1$	$\pm 100$		$\pm 1000$	$\pm 1000$	nA	
$I_{CC}$	$V_I = V_{CC}$ or 0, $I_O = 0$		6 V			8		160	80	$\mu\text{A}$	
$C_i$			2 V to 6 V		3	10		10	10	pF	



# SN54HC42, SN74HC42 4-LINE TO 10-LINE DECODERS (1 of 10)

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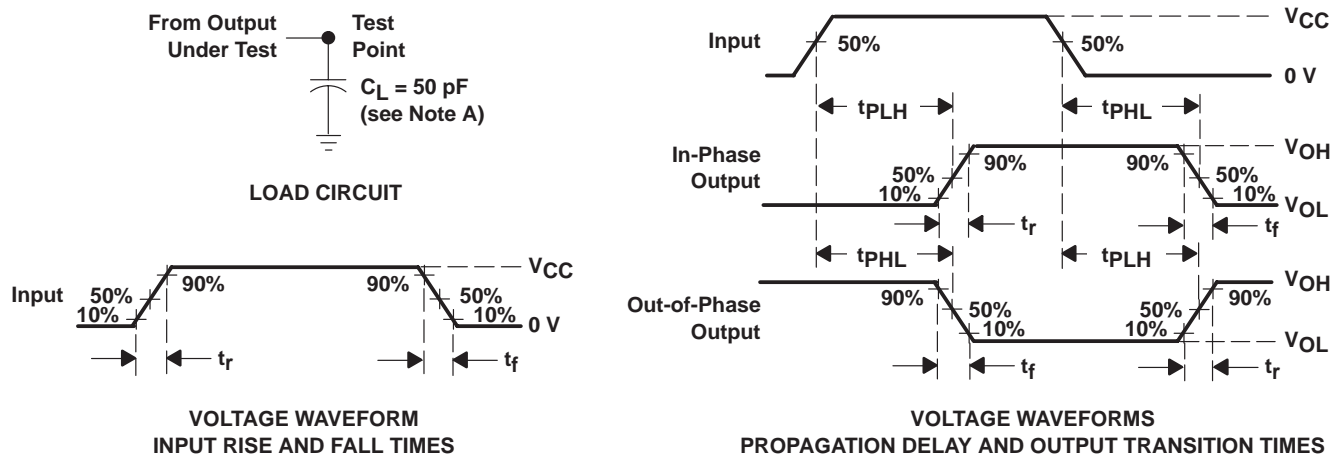
switching characteristics over recommended operating free-air temperature range,  $C_L = 50$  pF (unless otherwise noted) (see Figure 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	$V_{CC}$	$T_A = 25^\circ\text{C}$			SN54HC42		SN74HC42		UNIT
				MIN	TYP	MAX	MIN	MAX	MIN	MAX	
$t_{pd}$	A, B, C, or D	0–9	2 V		65	150		225		190	ns
			4.5 V		18	30		45		38	
			6 V		14	26		38		32	
$t_t$		Any	2 V		28	75		110		95	ns
			4.5 V		8	15		22		19	
			6 V		7	13		19		16	

operating characteristics,  $T_A = 25^\circ\text{C}$

PARAMETER	TEST CONDITIONS	TYP	UNIT
$C_{pd}$ Power dissipation capacitance	No load	39	pF

## PARAMETER MEASUREMENT INFORMATION



- NOTES:
- $C_L$  includes probe and test-fixture capacitance.
  - Phase relationships between waveforms were chosen arbitrarily. All input pulses are supplied by generators having the following characteristics:  $PRR \leq 1$  MHz,  $Z_O = 50 \Omega$ ,  $t_r = 6$  ns,  $t_f = 6$  ns.
  - The outputs are measured one at a time with one input transition per measurement.
  - $t_{PLH}$  and  $t_{PHL}$  are the same as  $t_{pd}$ .

**Figure 1. Load Circuit and Voltage Waveforms**

**PACKAGING INFORMATION**

Orderable Device	Status <sup>(1)</sup>	Package Type	Package Drawing	Pins	Package Qty	Eco Plan <sup>(2)</sup>	Lead/Ball Finish	MSL Peak Temp <sup>(3)</sup>
5962-86821012A	ACTIVE	LCCC	FK	20	1	TBD	Call TI	Level-NC-NC-NC
5962-8682101EA	ACTIVE	CDIP	J	16	1	TBD	Call TI	Level-NC-NC-NC
SN54HC42J	ACTIVE	CDIP	J	16	1	TBD	Call TI	Level-NC-NC-NC
SN74HC42D	ACTIVE	SOIC	D	16	40	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HC42DE4	ACTIVE	SOIC	D	16	40	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HC42DR	ACTIVE	SOIC	D	16	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HC42DRE4	ACTIVE	SOIC	D	16	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HC42DT	ACTIVE	SOIC	D	16	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HC42DTE4	ACTIVE	SOIC	D	16	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HC42N	ACTIVE	PDIP	N	16	25	Pb-Free (RoHS)	CU NIPDAU	Level-NC-NC-NC
SN74HC42NE4	ACTIVE	PDIP	N	16	25	Pb-Free (RoHS)	CU NIPDAU	Level-NC-NC-NC
SN74HC42NSR	ACTIVE	SO	NS	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HC42NSRE4	ACTIVE	SO	NS	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SNJ54HC42FK	ACTIVE	LCCC	FK	20	1	TBD	Call TI	Level-NC-NC-NC
SNJ54HC42J	ACTIVE	CDIP	J	16	1	TBD	Call TI	Level-NC-NC-NC

<sup>(1)</sup> The marketing status values are defined as follows:

**ACTIVE:** Product device recommended for new designs.

**LIFEBUY:** TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

**NRND:** Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBSELETE:** TI has discontinued the production of the device.

<sup>(2)</sup> Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS) or Green (RoHS & no Sb/Br) - please check <http://www.ti.com/productcontent> for the latest availability information and additional product content details.

**TBD:** The Pb-Free/Green conversion plan has not been defined.

**Pb-Free (RoHS):** TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

**Green (RoHS & no Sb/Br):** TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

<sup>(3)</sup> MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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J (R-GDIP-T\*\*)

14 LEADS SHOWN

CERAMIC DUAL IN-LINE PACKAGE



DIM \ PINS **	14	16	18	20
A	0.300 (7,62) BSC	0.300 (7,62) BSC	0.300 (7,62) BSC	0.300 (7,62) BSC
B MAX	0.785 (19,94)	.840 (21,34)	0.960 (24,38)	1.060 (26,92)
B MIN	—	—	—	—
C MAX	0.300 (7,62)	0.300 (7,62)	0.310 (7,87)	0.300 (7,62)
C MIN	0.245 (6,22)	0.245 (6,22)	0.220 (5,59)	0.245 (6,22)



4040083/F 03/03

- NOTES:
- All linear dimensions are in inches (millimeters).
  - This drawing is subject to change without notice.
  - This package is hermetically sealed with a ceramic lid using glass frit.
  - Index point is provided on cap for terminal identification only on press ceramic glass frit seal only.
  - Falls within MIL STD 1835 GDIP1-T14, GDIP1-T16, GDIP1-T18 and GDIP1-T20.



FK (S-CQCC-N\*\*)

LEADLESS CERAMIC CHIP CARRIER

28 TERMINAL SHOWN



- NOTES:
- A. All linear dimensions are in inches (millimeters).
  - B. This drawing is subject to change without notice.
  - C. This package can be hermetically sealed with a metal lid.
  - D. The terminals are gold plated.
  - E. Falls within JEDEC MS-004

N (R-PDIP-T\*\*)

PLASTIC DUAL-IN-LINE PACKAGE

16 PINS SHOWN



- NOTES:
- A. All linear dimensions are in inches (millimeters).
  - B. This drawing is subject to change without notice.
  - Falls within JEDEC MS-001, except 18 and 20 pin minimum body length (Dim A).
  - The 20 pin end lead shoulder width is a vendor option, either half or full width.

D (R-PDSO-G16)

PLASTIC SMALL-OUTLINE PACKAGE



- NOTES:
- A. All linear dimensions are in inches (millimeters).
  - B. This drawing is subject to change without notice.
  - C. Body dimensions do not include mold flash or protrusion not to exceed 0.006 (0,15).
  - D. Falls within JEDEC MS-012 variation AC.

# MECHANICAL DATA

NS (R-PDSO-G\*\*)

PLASTIC SMALL-OUTLINE PACKAGE

14-PINS SHOWN



- NOTES:
- A. All linear dimensions are in millimeters.
  - B. This drawing is subject to change without notice.
  - C. Body dimensions do not include mold flash or protrusion, not to exceed 0,15.

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**SN54HC42**, Status: ACTIVE

4-Line To 10-Line Decoders BCD To Decimal



clear gif

<input type="checkbox"/> Features	<input type="checkbox"/> Samples	<input type="checkbox"/> Technical Documents
<input type="checkbox"/> Quality & Pb-Free Data	<input type="checkbox"/> Pricing/Packaging	<input type="checkbox"/> Applications Notes
<input type="checkbox"/> Related Products	<input type="checkbox"/> Inventory	<input type="checkbox"/> Simulation Models
<input type="checkbox"/> Tools & Software	<input type="checkbox"/> Symbols/Footprints	<input type="checkbox"/> Reference Designs



**Refine Your Selection**

- Logic: Decoders/Demu

**Support**

- KnowledgeBase
- Contact Technical Supp
- TI Cross Reference
- Training
- Part Marking Lookup
- Part Number Nomencla

**Datasheet**



[Download Datasheet](#) **SN54HC42, SN74HC42 (Rev. D)** (sn54hc42.pdf, 420 KB)  
26 Sep 2003 [Download](#)

	SN54HC42	SN74HC42
<b>Voltage Nodes(V)</b>	6, 5, 2	6, 5, 2
<b>Vcc range(V)</b>	2.0 to 6.0	2.0 to 6.0
<b>Input Level</b>	CMOS	CMOS
<b>Output Level</b>	CMOS	CMOS
<b>Output Drive(mA)</b>		-4/4
<b>Output</b>	2S	2S
<b>From</b>	4	4
<b>To</b>	10	10
	<a href="#">Samples</a>	<a href="#">Samples</a>
	<a href="#">Inventory</a>	<a href="#">Inventory</a>

**Product Information**

Features [Save this to your personal library](#)

- Wide Operating Voltage Range of 2 V to 6 V
- Outputs Can Drive Up To 10 LSTTL Loads
- Low Power Consumption, 80-µA Max I<sub>CC</sub>
- Typical t<sub>pd</sub> = 14 ns
- ±4-mA Output Drive at 5 V
- Low Input Current of 1 µA Max
- Full Decoding of Input Logic
- All Outputs Are High for Invalid BCD Conditions
- Also for Applications as 3-Line to 8-Line Decoders

Description

These decimal decoders consist of eight inverters and ten 4-input NAND gates. The inverters are connected in pairs to make BCD input data available for decoding by the NAND gates. Full decoding of valid input logic ensures that all inputs remain off for all invalid input conditions.

**Pricing/Packaging/CAD Design Tools/Samples**


				Price	Packaging	CAD Design Tools	Samples	
Device	Status	Temp (°C)	DSCC #	Budget Price (\$US)   QTY	Industry Standard (TI Pkg)   Pins	Standard Pack Quantity	Footprints	Samples
5962-86821012A	ACTIVE	-55 to 125		13.29   1KU	LCCC (FK)   20	1	<input type="checkbox"/>	<a href="#">Request Military Samples</a>
5962-8682101EA	ACTIVE	-55 to 125		3.72   1KU	CDIP (J)   16	1	<input type="checkbox"/>	<a href="#">Request Military Samples</a>
SN54HC42J	ACTIVE	-55 to 125		2.05   1KU	CDIP (J)   16	1	<input type="checkbox"/>	<a href="#">Request Military Samples</a>
SNJ54HC42FK	ACTIVE	-55 to 125	5962-86821012A	13.29   1KU	LCCC (FK)   20	1	<input type="checkbox"/>	<a href="#">Request Military Samples</a>
SNJ54HC42J	ACTIVE	-55 to 125	5962-8682101EA	3.72   1KU	CDIP (J)   16	1	<input type="checkbox"/>	<a href="#">Request Military Samples</a>

**Inventory**

TI Inventory Status				Reported Distributor Inventory			
As of 9:24 AM GMT, 25 Nov 2005				As of 9:24 AM GMT, 25 Nov 2005			
<b>5962-86821012A</b>	<b>In Stock</b>	<b>In Progress QTY   Date</b>	<b>Lead Time</b>	<b>Region</b>	<b>Company</b>	<b>In Stock</b>	<b>Purchase</b>
	85*		8 Weeks	Americas	Rochester Electronics	56	<input type="text"/>
As of 9:24 AM GMT, 25 Nov 2005				As of 9:24 AM GMT, 25 Nov 2005			
<b>5962-8682101EA</b>	<b>In Stock</b>	<b>In Progress QTY   Date</b>	<b>Lead Time</b>	<b>Region</b>	<b>Company</b>	<b>In Stock</b>	<b>Purchase</b>
	196*	>10k   19 Dec	8 Weeks	None Reported <a href="#">View Distributors</a>			
As of 9:24 AM GMT, 25 Nov 2005				As of 9:24 AM GMT, 25 Nov 2005			
<b>SN54HC42J</b>	<b>In Stock</b>	<b>In Progress QTY   Date</b>	<b>Lead Time</b>	<b>Region</b>	<b>Company</b>	<b>In Stock</b>	<b>Purchase</b>
	191*	>10k   19 Dec	8 Weeks	Europe	EBV Elektronik	49	<input type="text"/>
As of 9:24 AM GMT, 25 Nov 2005				As of 9:24 AM GMT, 25 Nov 2005			
<b>SNJ54HC42FK</b>	<b>In Stock</b>	<b>In Progress QTY   Date</b>	<b>Lead Time</b>	<b>Region</b>	<b>Company</b>	<b>In Stock</b>	<b>Purchase</b>
	85*		8 Weeks	None Reported <a href="#">View Distributors</a>			
As of 9:24 AM GMT, 25 Nov 2005				As of 9:24 AM GMT, 25 Nov 2005			
<b>SNJ54HC42J</b>	<b>In Stock</b>	<b>In Progress QTY   Date</b>	<b>Lead Time</b>	<b>Region</b>	<b>Company</b>	<b>In Stock</b>	<b>Purchase</b>
	196*	>10k   19 Dec	8 Weeks	Europe	Arrow Northern Europe	712	<input type="text"/>
					Arrow Southern Europe	93	<input type="text"/>

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**Quality & Lead (Pb)-Free Data**

		Product Content			MTBF/FIT Rate	
Device	Eco Plan*	Lead/Ball Finish	MSL Rating/Peak Reflow	Details	Details	
5962-86821012A	TBD	Call TI	Level-NC-NC-NC	<a href="#">View</a>	<a href="#">View</a>	
5962-8682101EA	TBD	Call TI	Level-NC-NC-NC	<a href="#">View</a>	<a href="#">View</a>	
SN54HC42J	TBD	Call TI	Level-NC-NC-NC	<a href="#">View</a>	<a href="#">View</a>	
SNJ54HC42FK	TBD	Call TI	Level-NC-NC-NC	<a href="#">View</a>	<a href="#">View</a>	
SNJ54HC42J	TBD	Call TI	Level-NC-NC-NC	<a href="#">View</a>	<a href="#">View</a>	

\* The planned eco-friendly classification: Pb-Free (RoHS) or Green (RoHS & no Sb/Br) - please click on the Product Content Details "View" link in the table above for the latest availability information and additional product content details.

If the information you are requesting is not available online at this time, contact one of our [Product Information Centers](#) regarding the availability of this information.

**SN54HC42, SN74HC42 (Rev. D)** (sn54hc42.pdf, 420 KB)

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**Shelf-Life Evaluation of Lead-Free Component Finishes** (szza046.htm, 9 KB)

24 May 2004 [Abstract](#)

**Understanding and Interpreting Standard-Logic Data Sheets (Rev. B)** (szza036b.htm, 8 KB)

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**TI IBIS File Creation, Validation, and Distribution Processes** (szza034.htm, 9 KB)

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**LOGIC Pocket Data Book** (scyd013.pdf, 4835 KB)

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**Logic Selection Guide 2005 (Rev. X)** (sdyu001x.pdf, 6909 KB)

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**Military Semiconductors Selection Guide 2004-2005 (Rev. D)** (sgyc003d.pdf, 964 KB)

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**SN74HC4851/HC4852 Product Clip (Rev. B)** (scyb019b.pdf, 501 KB)

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**Logic Cross-Reference (Rev. A)** (scyb017a.pdf, 2938 KB)

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